

# **Inverting the Bipolar Differential Pair for Low-Voltage Applications**

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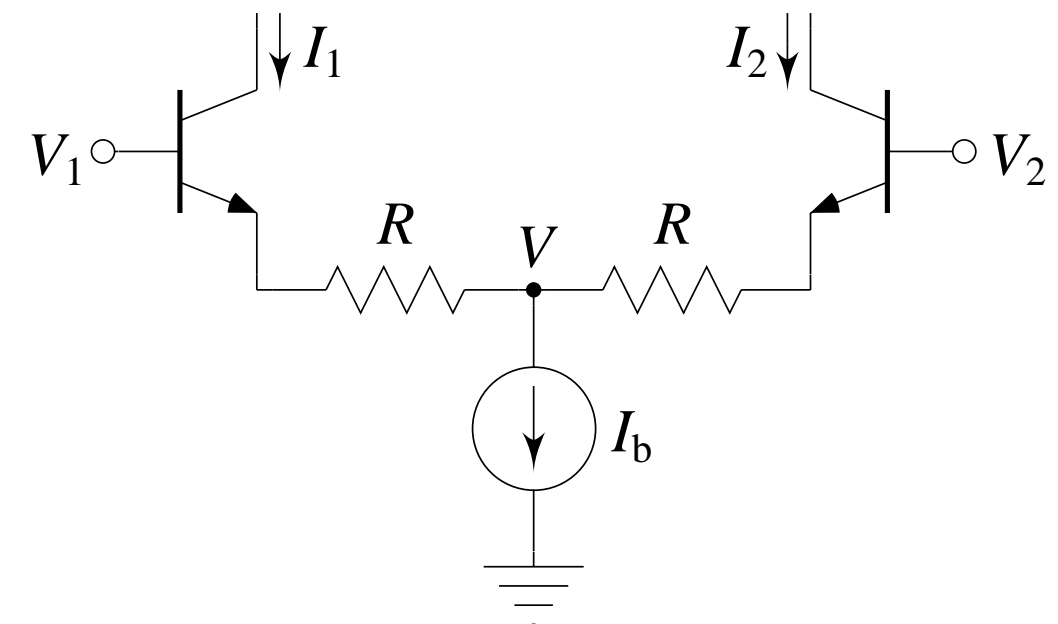
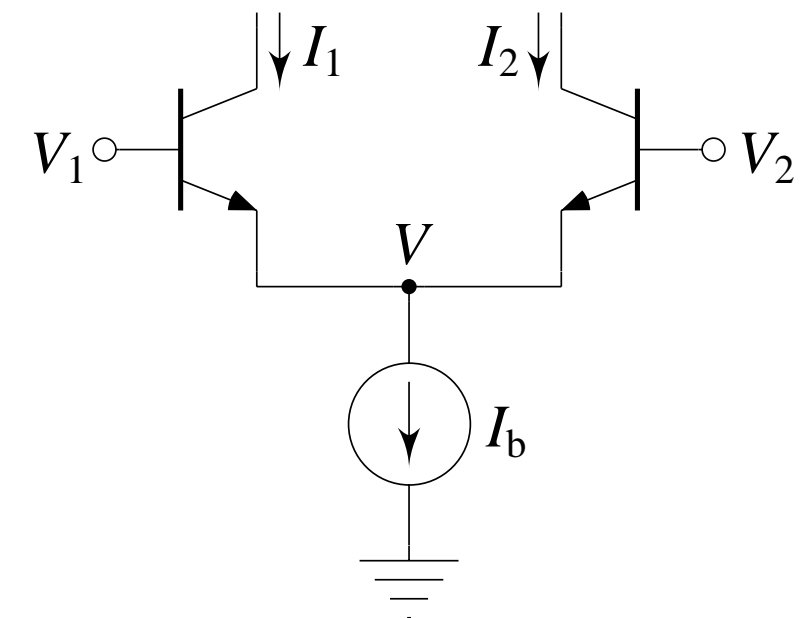
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The logo consists of a solid red square with the word "CORNELL" written in white, uppercase, serif font centered within it.

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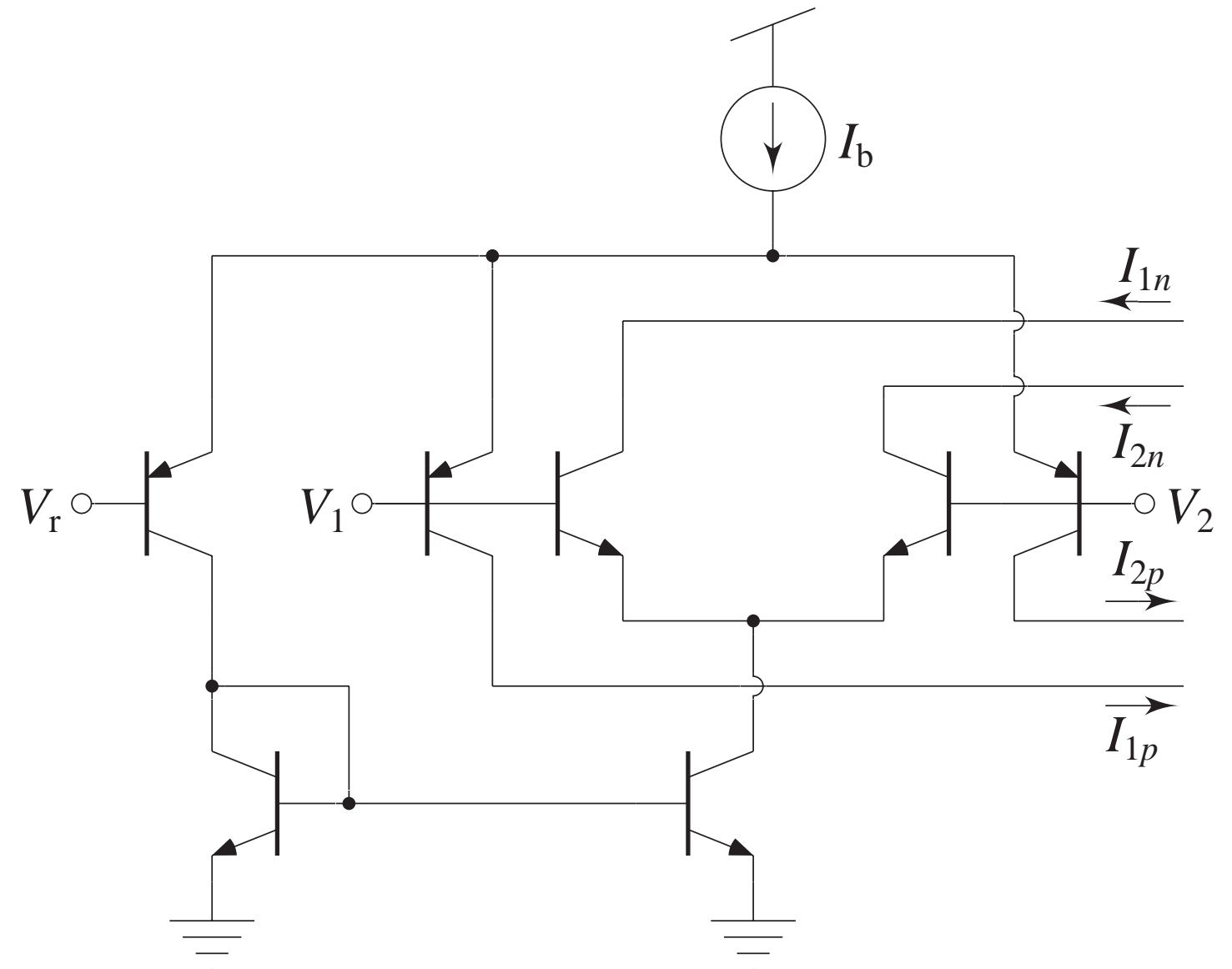
# Differential Pairs

- The differential pair is used ubiquitously, but does not perform well with a low supply voltage.
- The minimum allowable  $V_{cm}$  is approximately  $V_{diode} + V_{CEsat}$ .
- To improve linearity, we introduce emitter-degeneration resistors, which increase the linear range from a few  $U_T$  to about  $I_b R$ .
- However, the minimum allowable  $V_{cm}$  is also increased to about  $V_{diode} + I_b R + V_{CEsat}$ .
- We present a new bipolar differential pair with comparable linearity for which this voltage is the minimum allowable supply voltage.



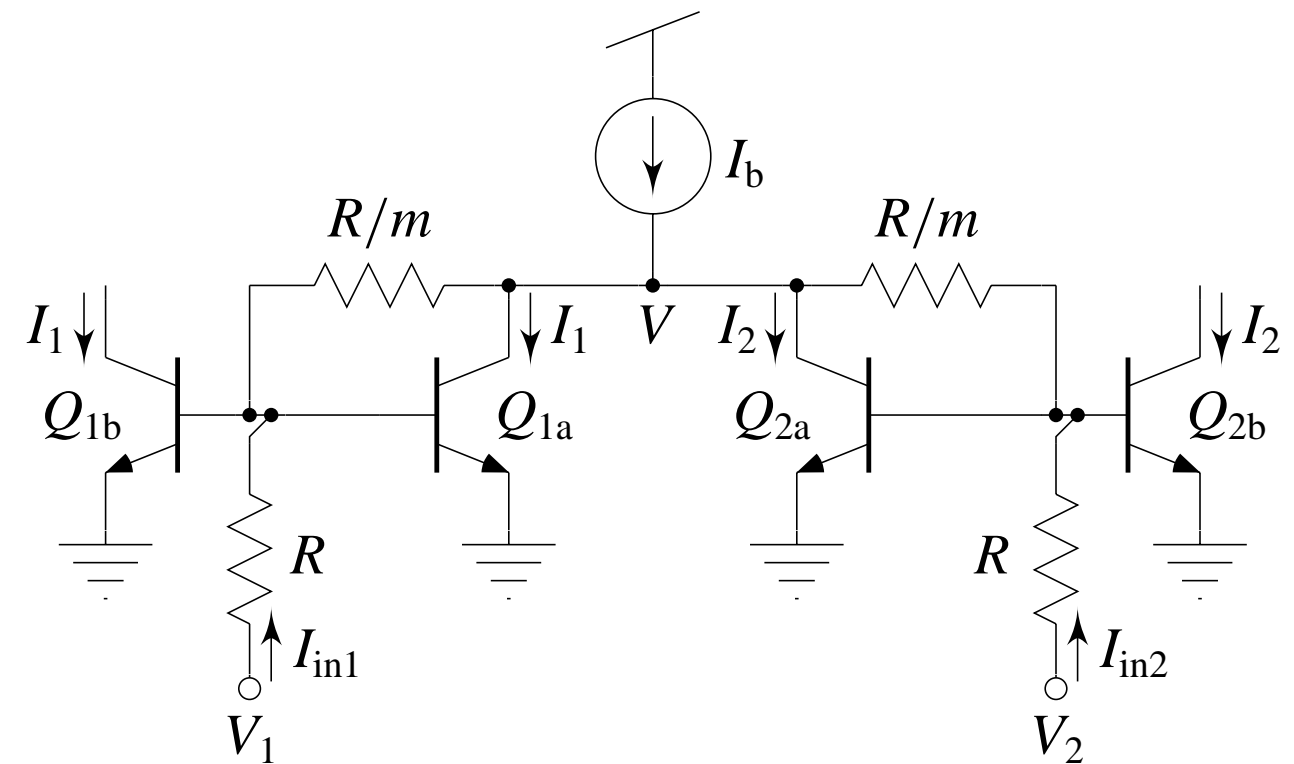
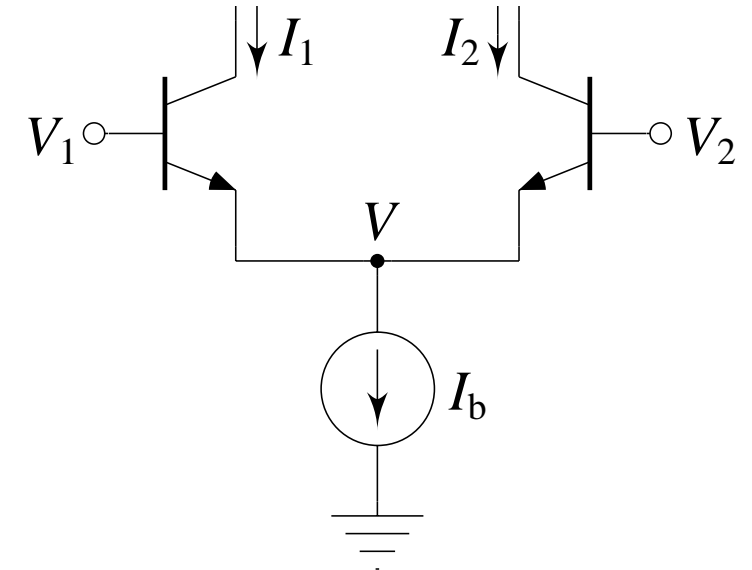
## Conventional Rail-to-Rail Differential Input Stages

- The *pn*p differential pair has a complementary common-mode input-voltage range to the *npn* version.
- To build a rail-to-rail input stage, we can use a differential pair of each type, combining  $I_{1n}$  with  $I_{2p}$  and  $I_{2n}$  with  $I_{1p}$ .
- To get a constant differential transconductance, we must control the tail-currents.
- The minimum supply voltage for such a circuit is  $2V_{\text{diode}} + 2V_{\text{CEsat}}$ .



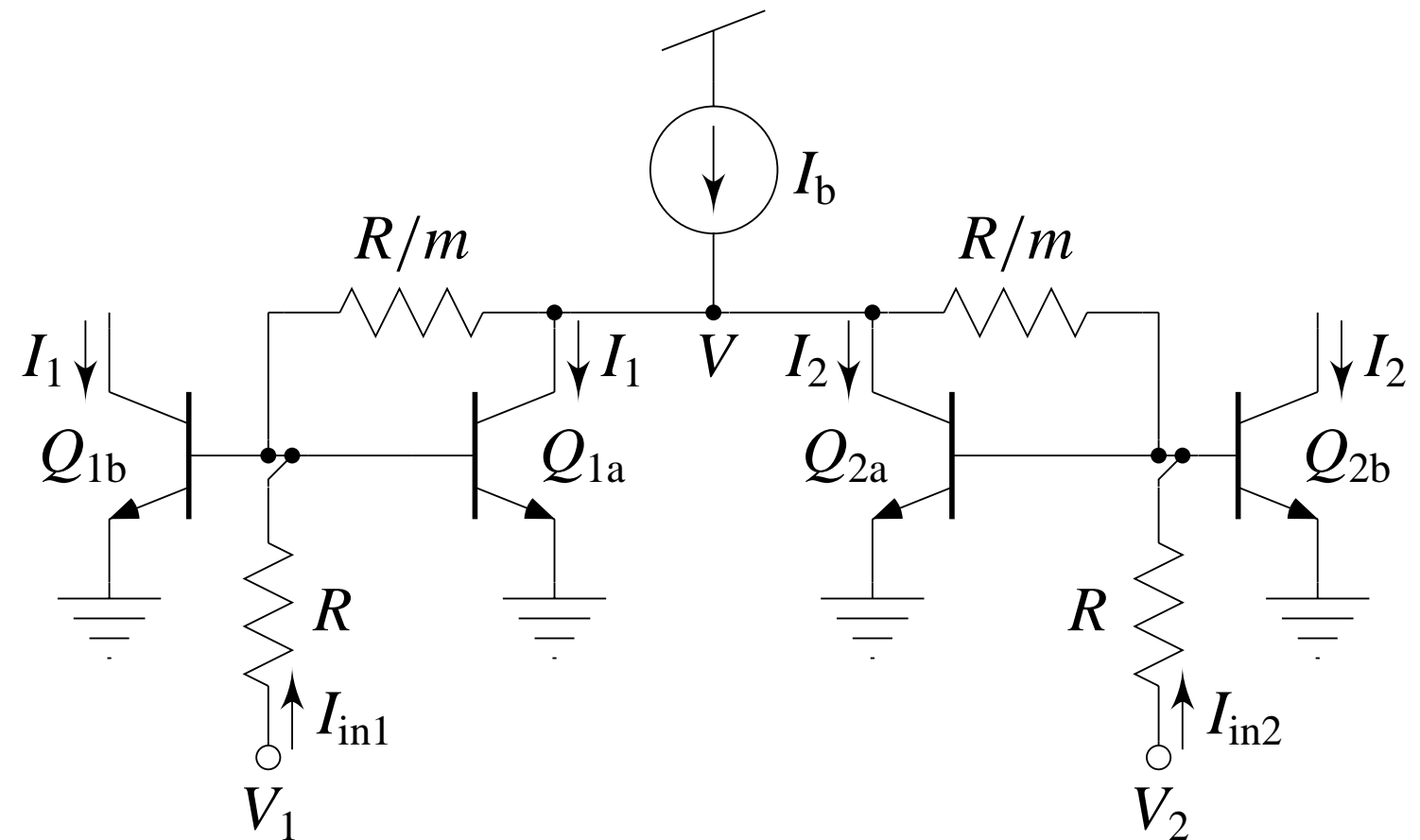
## Inverted Differential Pair

- In the conventional differential pair, we maintain the transistors' transconductances nearly constant despite large changes in  $V_{cm}$  by keeping  $I_1 + I_2 = I_b$ .
- A current comparison between  $I_1 + I_2$  and  $I_b$  occurs implicitly at node  $V$ .  $V$  adjusts itself until  $I_1 + I_2 = I_b$ .
- In the *inverted differential pair*, we also maintain  $I_1 + I_2 = I_b$  using local feedback, except that we do so *indirectly* using replicas of the output currents.
- If the current flowing in the resistors is small,  $I_1 + I_2 \approx I_b$ .



## Inverted Differential Pair

- $V$  attains its maximal value when  $V_{cm}$  is at its minimum.
- If  $V_{cm}$  increases,  $V$  decreases by about  $1/m$  times as much to compensate the increase in  $I_1 + I_2$ .
- By making  $m = 2$  or  $3$ , we compress the swing on  $V$ , leaving ample headroom to keep  $Q_{1a}$  and  $Q_{1b}$  active.
- $Q_{1b}$  and  $Q_{2b}$  have their emitters at ground, permitting a wide output swing.



- The bases are fixed by feedback, making  $R_{in} \approx R$ . We can also use them as summing points, coupling in additional inputs.

## Behaves Like an Emitter-Degenerated Differential Pair

- Emitter-degenerated differential pair

$$I_1 = I_s e^{(V_1 - V)/U_T} e^{-\alpha I_1 R / U_T}$$

$$I_2 = I_s e^{(V_2 - V)/U_T} e^{-\alpha I_2 R / U_T}$$

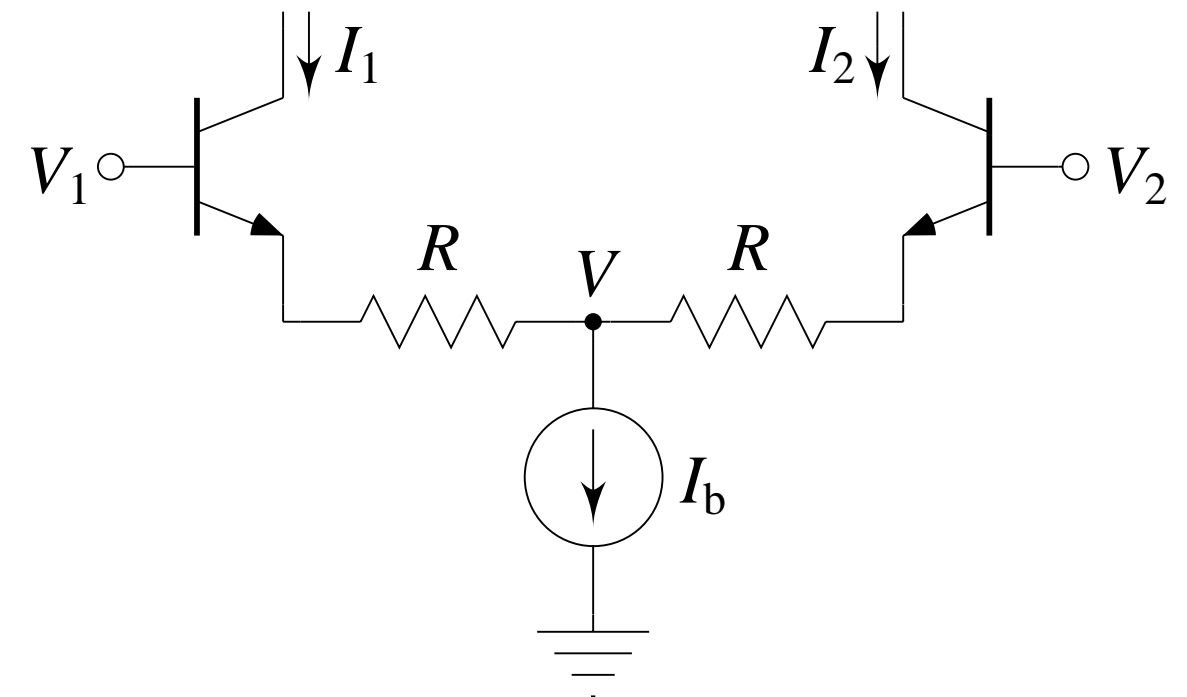
$$I_b = \alpha (I_1 + I_2),$$

- Inverted differential pair

$$I_1 = I_s e^{(V_1 + V)/(1+m)U_T} e^{-2I_1 R / \beta(1+m)U_T}$$

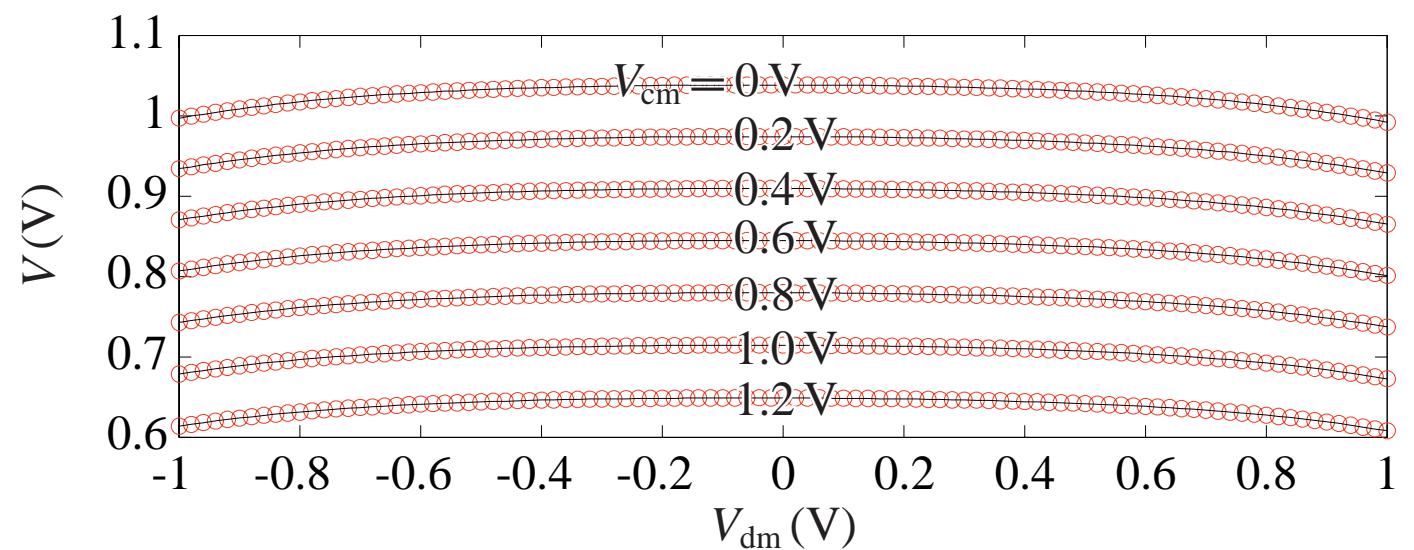
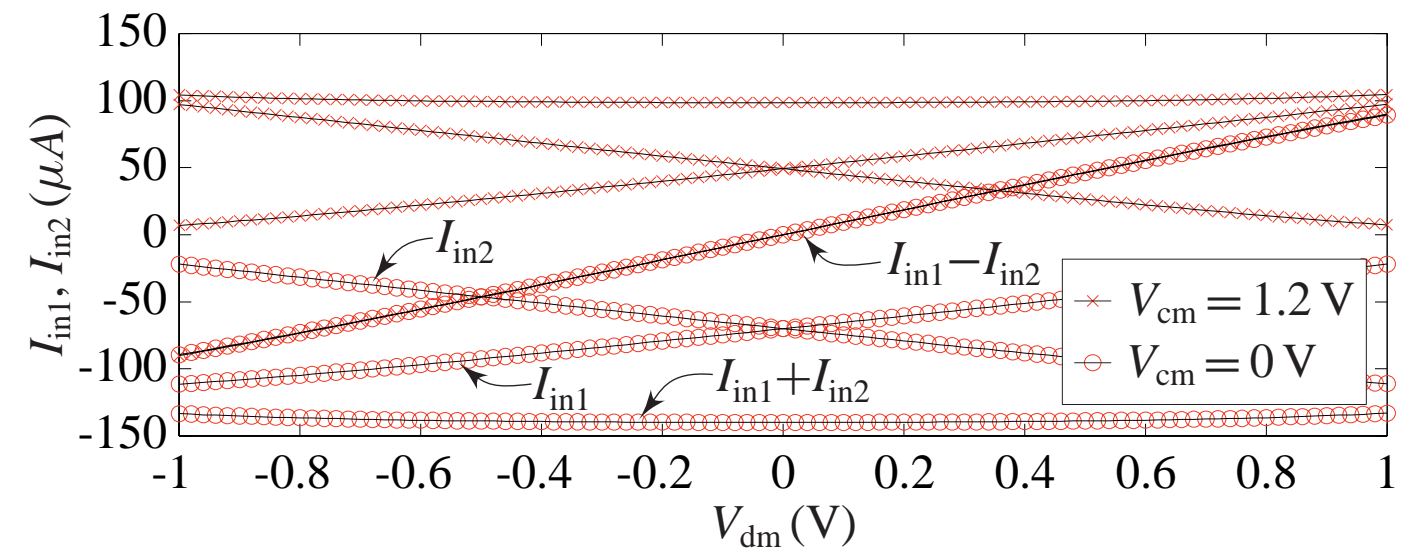
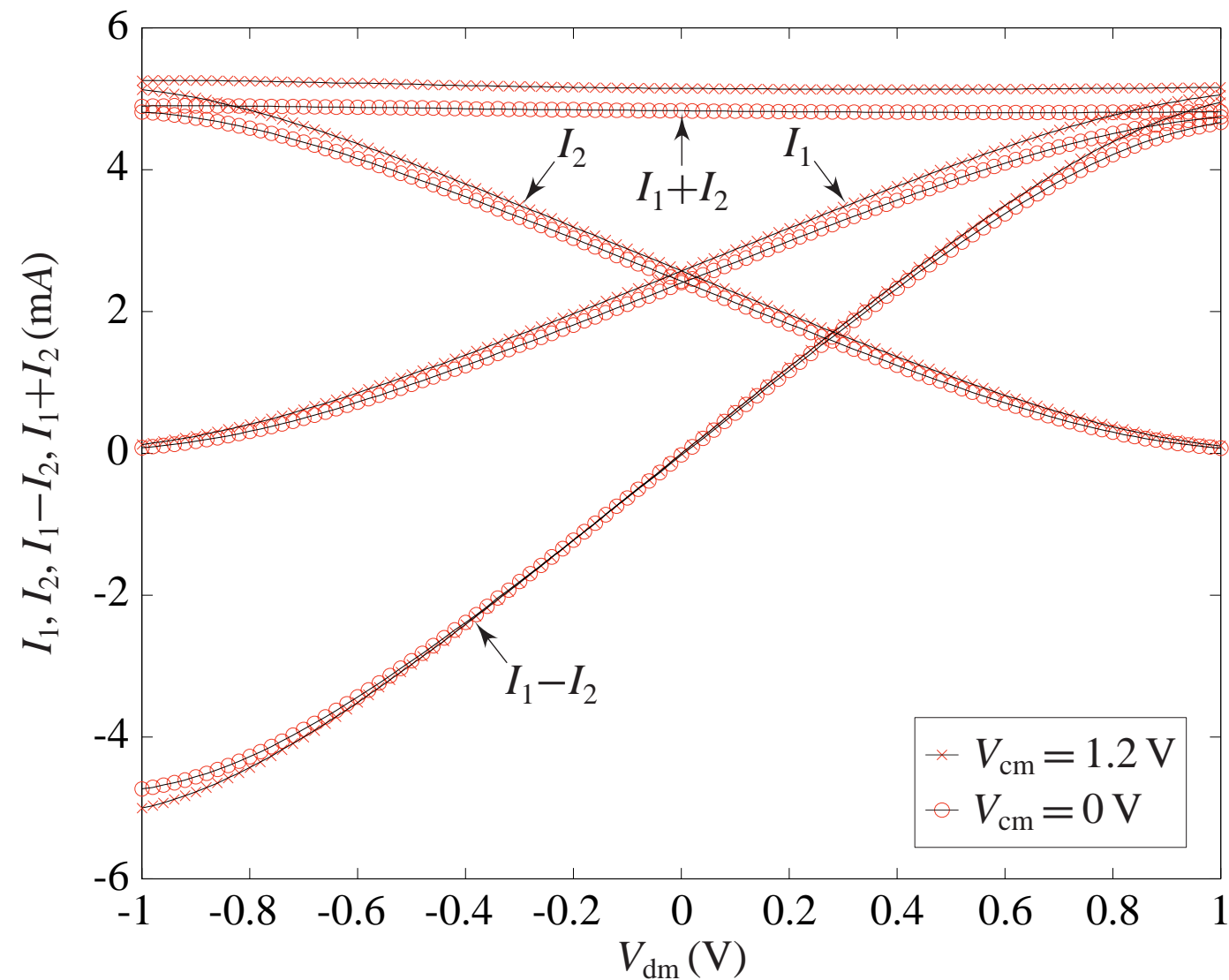
$$I_2 = I_s e^{(V_2 + V)/(1+m)U_T} e^{-2I_2 R / \beta(1+m)U_T}$$

$$I_b = \left(1 + \frac{2}{\beta}\right) (I_1 + I_2) - (I_{in1} + I_{in2}),$$



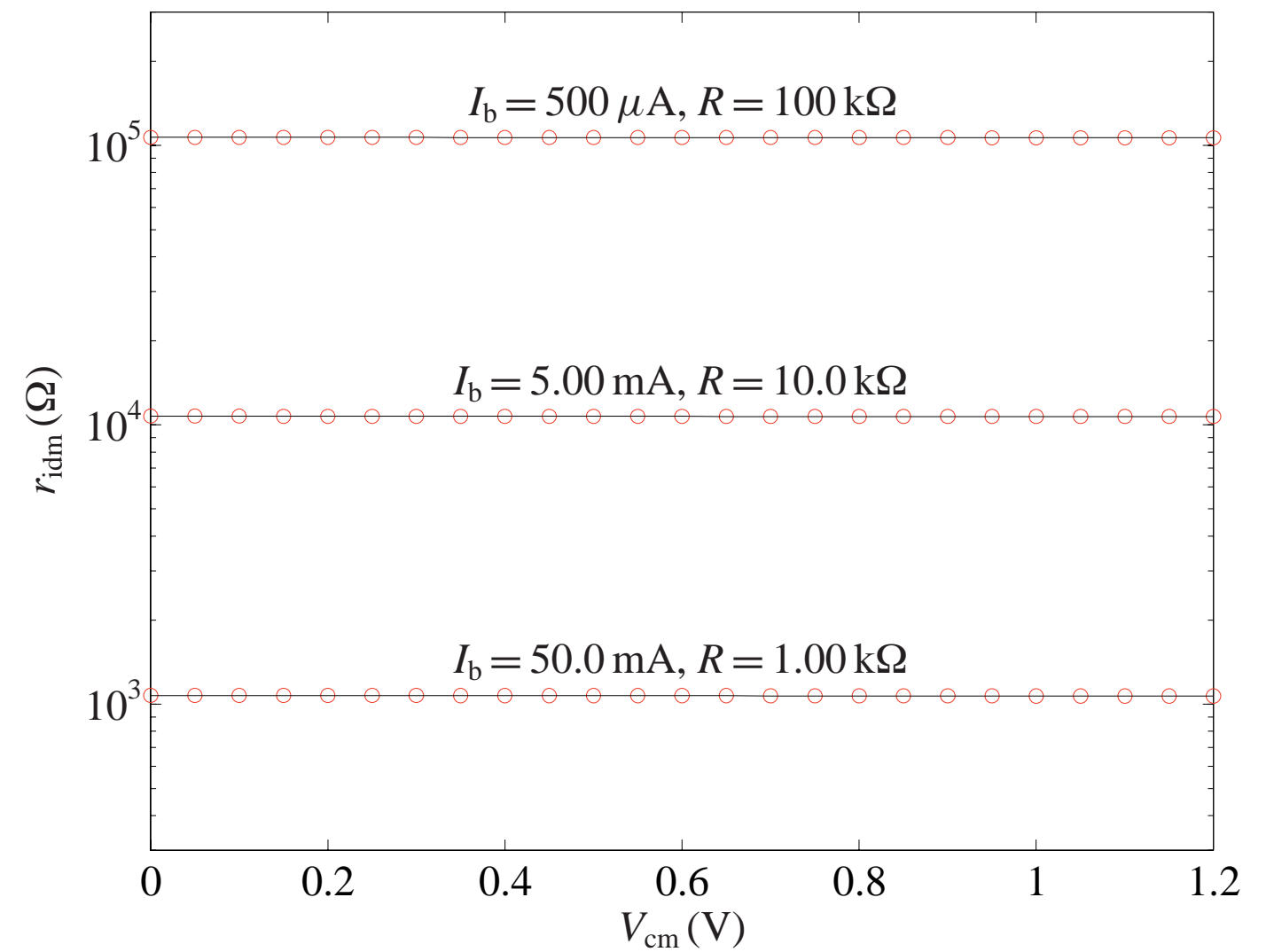
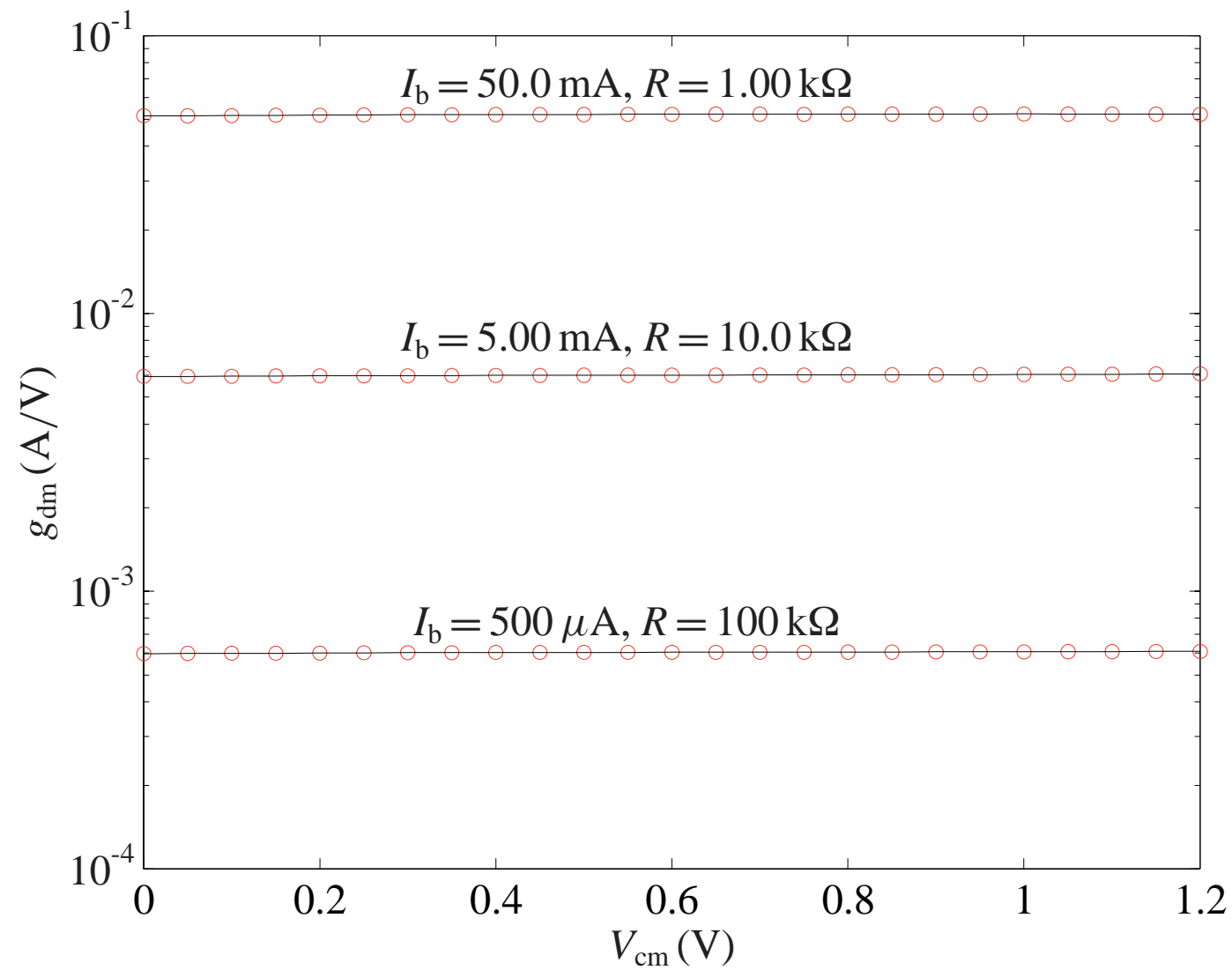
- If  $I_{in1,2} \ll I_b$ , these systems are isomorphic with  $-V \rightarrow V$ ,  $U_T \rightarrow (1+m)U_T$ , and  $\alpha R \rightarrow 2R/\beta$ .

# Measured DC Characteristics with $I_b = 5 \text{ mA}$ and $R = 10 \text{ k}\Omega$





# Measured Differential Transconductance Gain and Input Resistance





## Summary

We have presented a new bipolar differential transconductor that functions just like an emitter-degenerated differential pair except that it

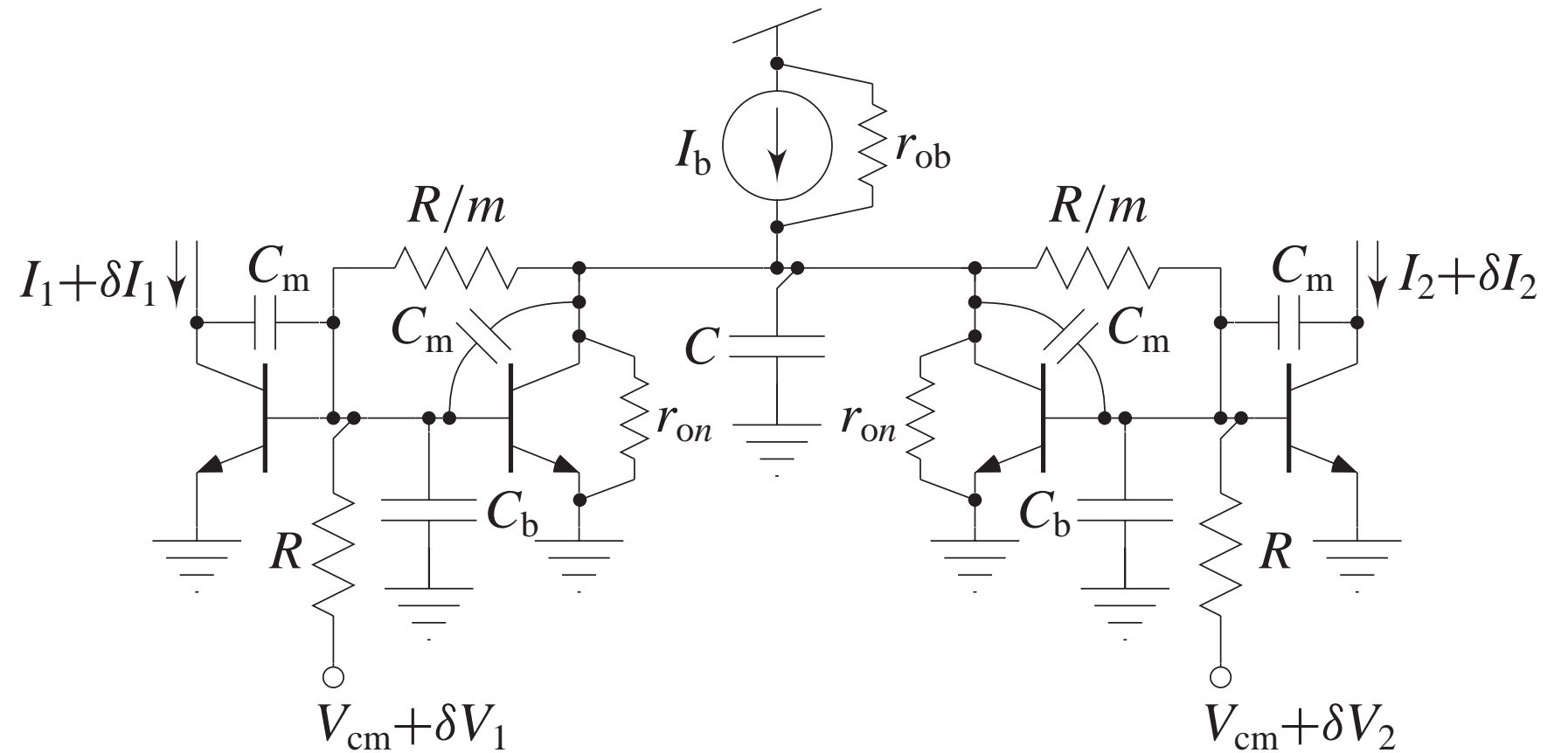
- can operate on a low power supply voltage,
- has a rail-to-rail common-mode input-voltage range,
- permits a wide output-voltage swing,
- has a differential transconductance gain that is nearly independent of  $V_{cm}$ , and
- requires only *npn* transistors in the signal path.

We provided DC measurements from a prototype circuit, breadboarded from a quad TPQ3904 and a thick-film resistor array, demonstrating operation on a single-ended 1.2-V power supply.



# Inverted Differential Pair Frequency Response

- We use the circuit shown to the right to compute the frequency response of the inverted differential pair.
- Assuming that  $C \gg C_m$ ,  $C_b \gg C_m$ ,  $\beta \gg 1$ ,  $g_m R \gg 1$ , and  $g_m (r_{on} \parallel 2r_{ob}) \gg 1$ , we can show that



$$G_{dm} \equiv \frac{\delta I_{1,2}}{\delta V_{dm}} = \pm \frac{1}{2} \cdot \frac{g_m}{1 + m + 2g_m R/\beta} \cdot \frac{1 - \tau_3 s}{1 + \tau_1 s},$$

## Inverted Differential Pair Frequency Response

$$G_{\text{cm}} \equiv \frac{\delta I_{1,2}}{\delta V_{\text{cm}}} = \frac{1}{m} \cdot \frac{1}{(R/m \parallel r_{\text{on}} \parallel 2r_{\text{ob}})} \cdot \frac{(1 - \tau_3 s)(1 + \tau_2 s)}{1 + (\tau_4^2 / (\tau_1 \parallel \tau_2))s + \tau_4^2 s^2},$$

and

$$\text{CMRR} \equiv \frac{G_{\text{dm}}}{G_{\text{cm}}} = \frac{m}{2} \cdot \frac{g_m (R/m \parallel r_{\text{on}} \parallel 2r_{\text{ob}})}{1 + m + 2g_m R/\beta} \cdot \frac{1 + (\tau_4^2 / (\tau_1 \parallel \tau_2))s + \tau_4^2 s^2}{(1 + \tau_1 s)(1 + \tau_2 s)},$$

where

$$\begin{aligned} \tau_1 &\equiv (R / (1 + m) \parallel \beta / 2g_m) C_b & \tau_3 &\equiv C_m / g_m \\ \tau_2 &\equiv (R / m \parallel r_{\text{on}} \parallel r_{\text{ob}}) C / 2 & \tau_4 &\equiv \sqrt{(C_b / g_m) (RC / 2m)}. \end{aligned}$$