

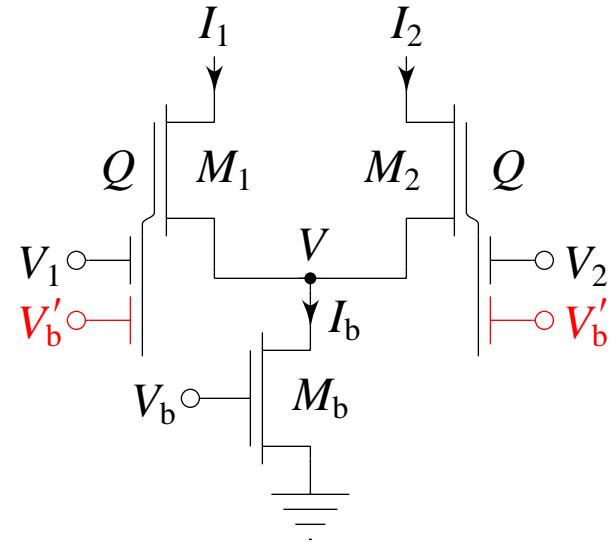
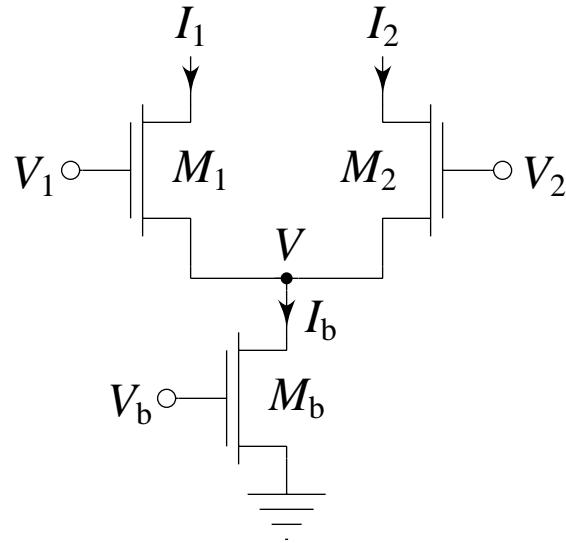
Evolution of a Folded Floating-Gate Differential Pair

Bradley A. Minch

Mixed Analog-Digital VLSI Circuits and Systems Laboratory
School of Electrical and Computer Engineering
Cornell University
Ithaca, NY 14853-5401

minch@ee.cornell.edu
<http://www.ee.cornell.edu/~minch>

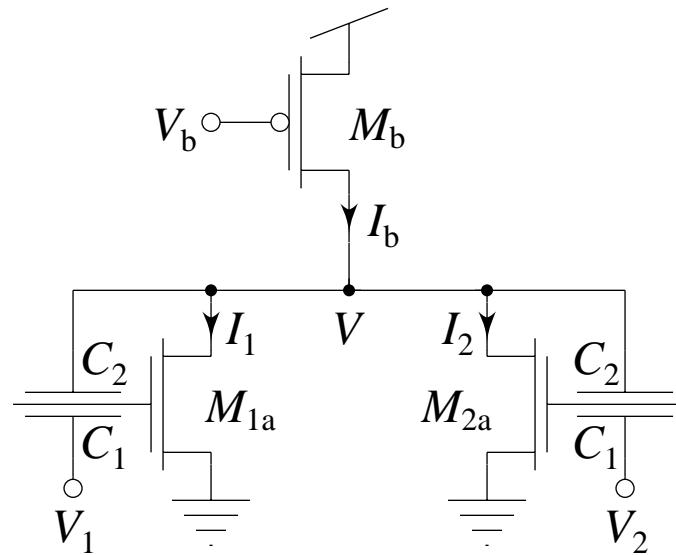
Conventional Differential Pairs



Differential-pair intuition:

- ▶ $I_1 = f(V_1, -V)$ and $I_2 = f(V_2, -V)$, where f is expansive.
- ▶ V adjusts itself so that $I_1 + I_2 \rightarrow I_b$.

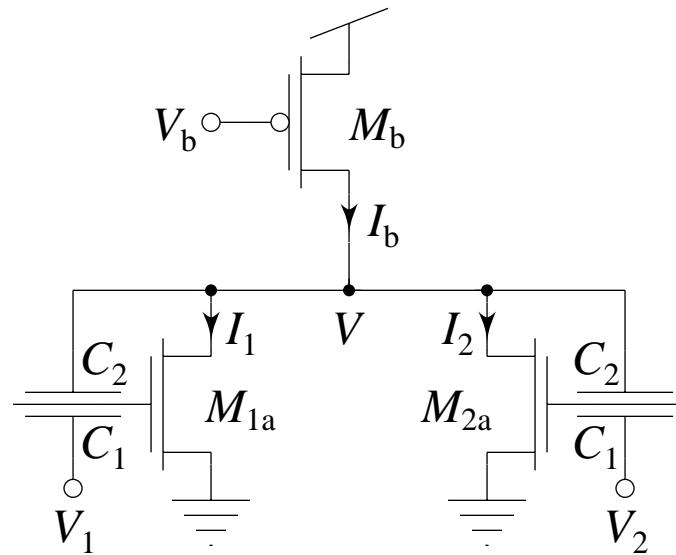
A Folded Floating-Gate Differential Pair



Differential-pair intuition:

- ▶ $I_1 = f(V_1, V)$ and $I_2 = f(V_2, V)$, where f is expansive.
- ▶ V adjusts itself so that $I_1 + I_2 \rightarrow I_b$.

A Folded Floating-Gate Differential Pair

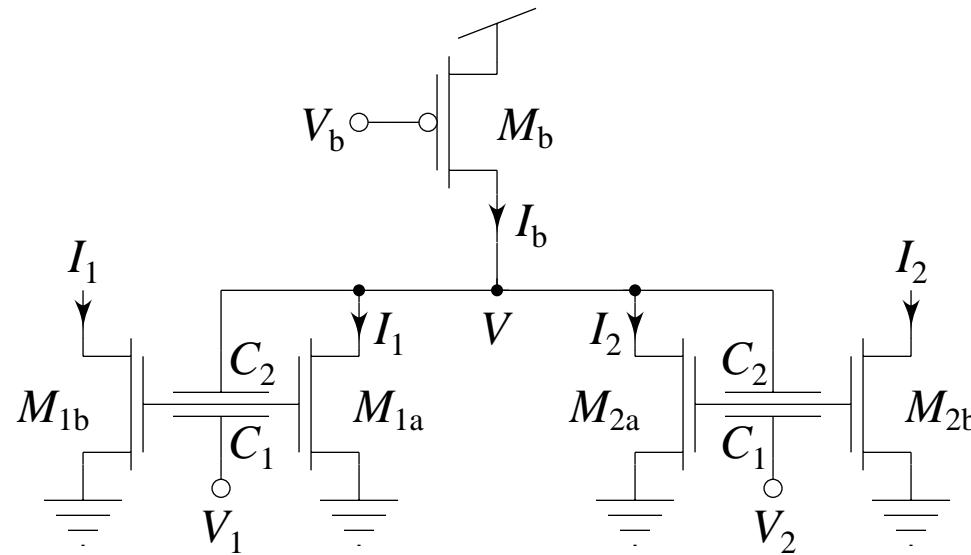


Differential-pair intuition:

- ▶ $I_1 = f(V_1, V)$ and $I_2 = f(V_2, V)$, where f is expansive.
- ▶ V adjusts itself so that $I_1 + I_2 \rightarrow I_b$.

Sign difference permits us to *fold* M_b relative to M_1 and M_2 .

A Folded Floating-Gate Differential Pair

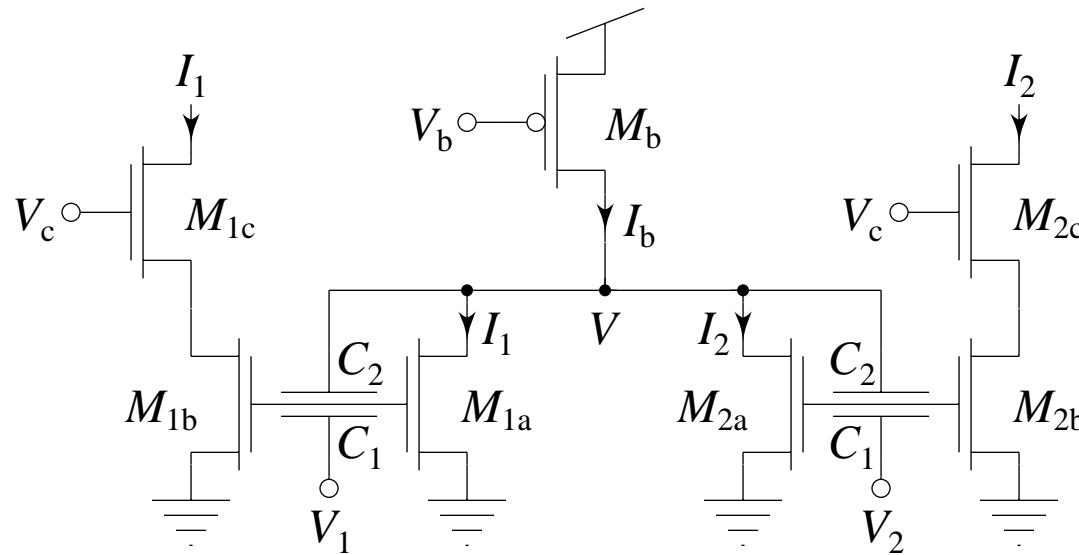


Differential-pair intuition:

- ▶ $I_1 = f(V_1, V)$ and $I_2 = f(V_2, V)$, where f is expansive.
- ▶ V adjusts itself so that $I_1 + I_2 \rightarrow I_b$.

M_{1b} and M_{2b} provide mirror copies of I_1 and I_2 .

A Folded Floating-Gate Differential Pair

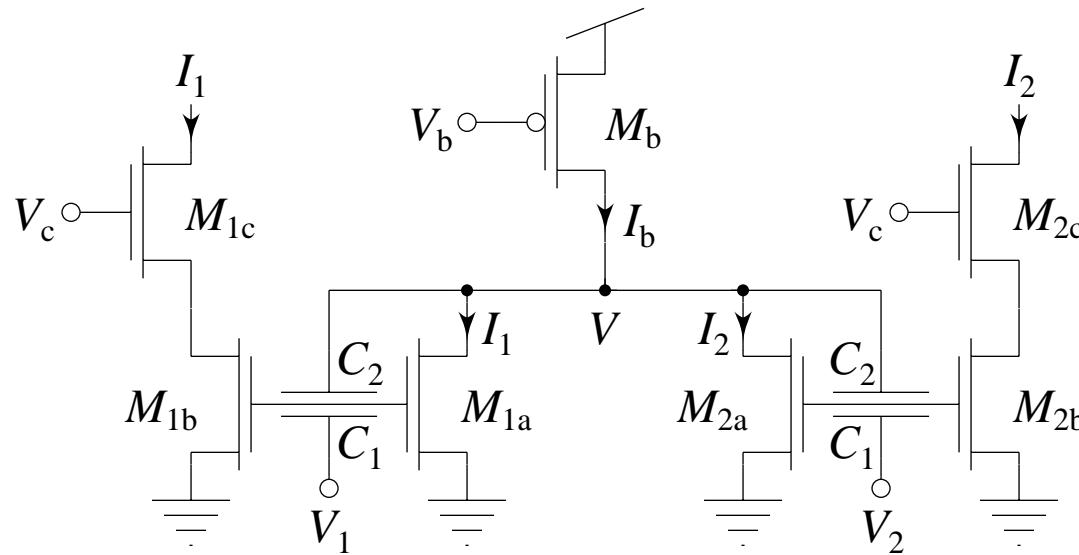


Differential-pair intuition:

- ▶ $I_1 = f(V_1, V)$ and $I_2 = f(V_2, V)$, where f is expansive.
- ▶ V adjusts itself so that $I_1 + I_2 \rightarrow I_b$.

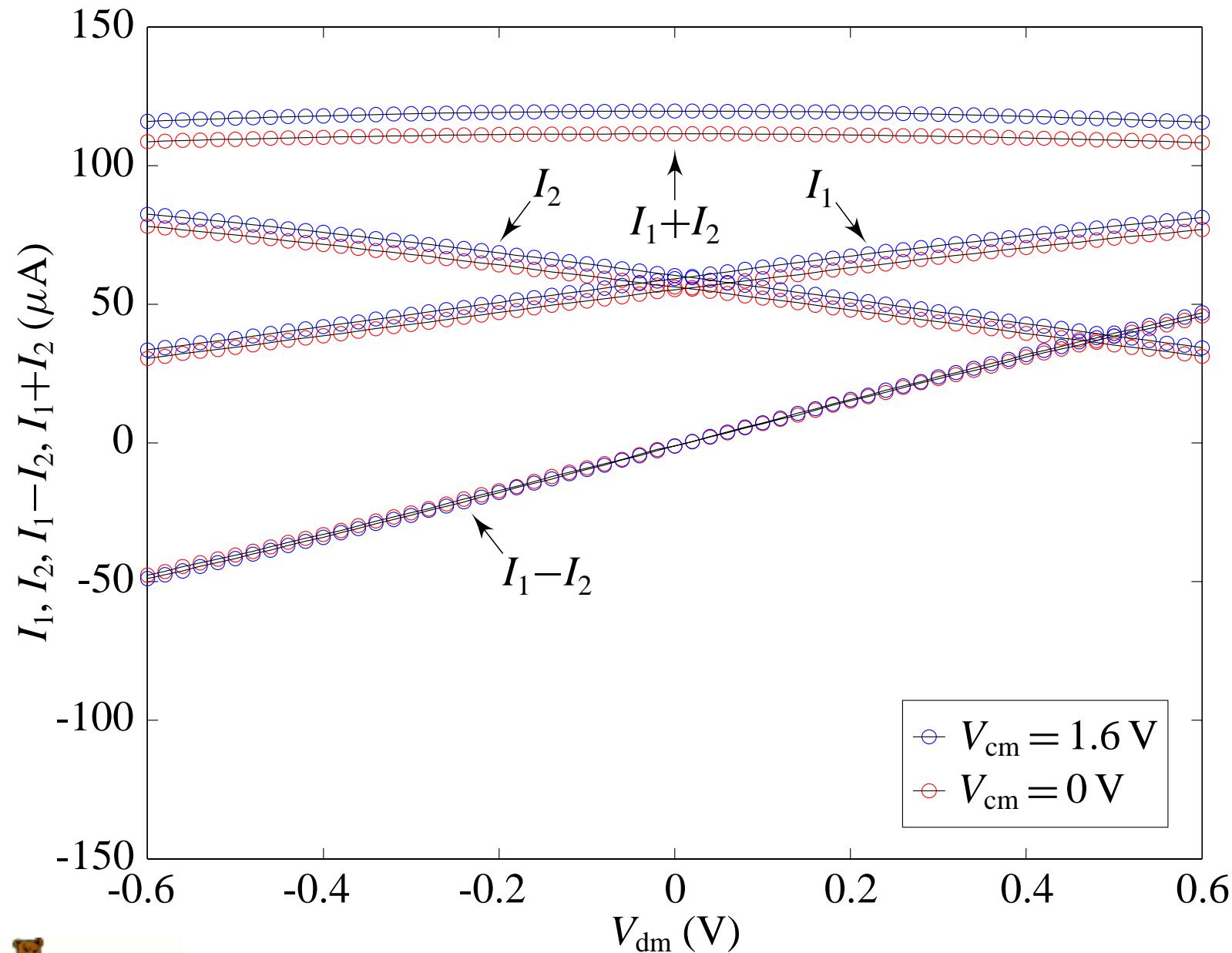
M_{1c} and M_{2c} mitigate the C_{gd} 's of transistors M_{1b} and M_{2b} .

A Folded Floating-Gate Differential Pair

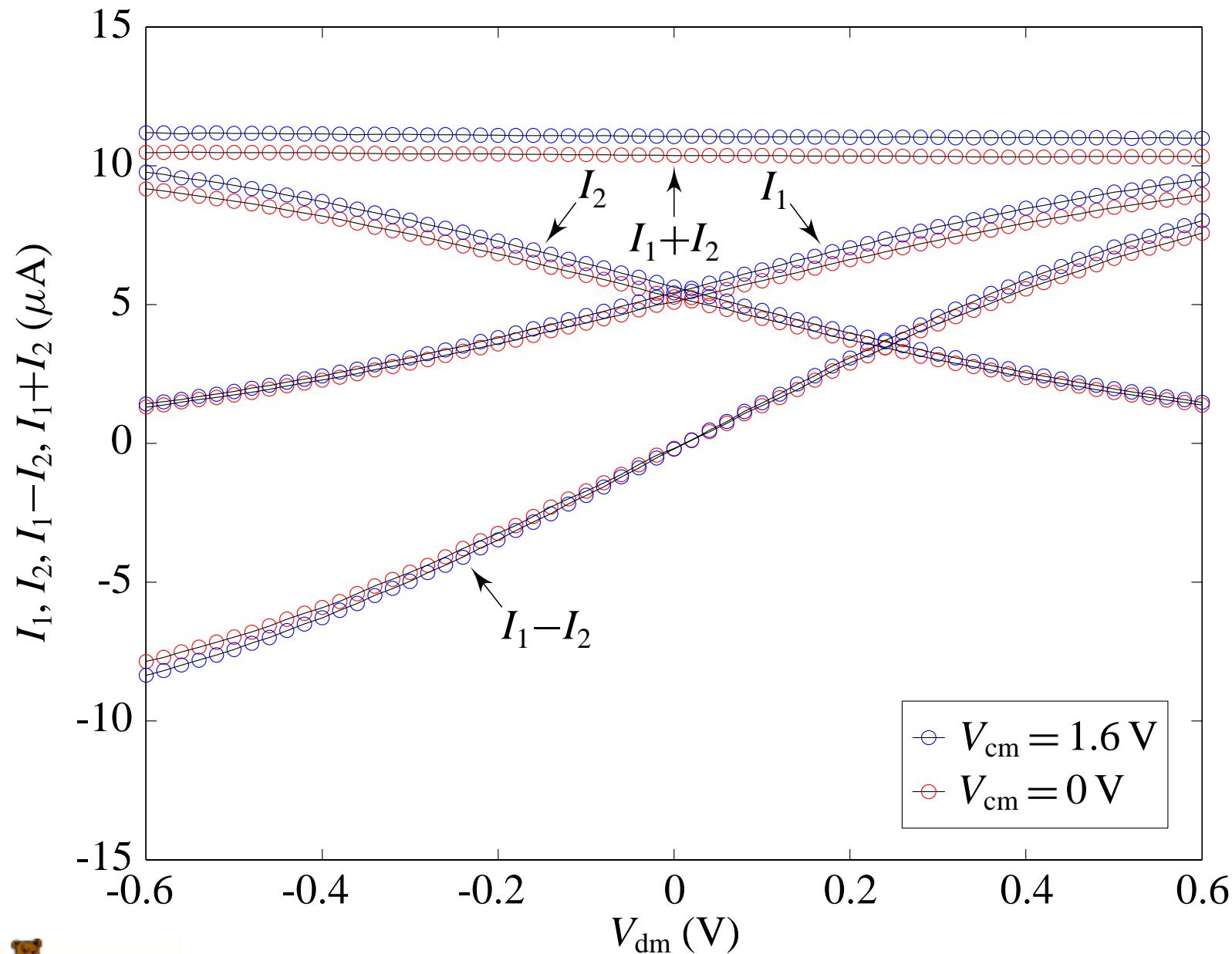


- ▶ C_1 sets the linear range and transconductance gain.
- ▶ C_2 controls by how much V changes in response to changes in either V_{cm} or I_b .
- ▶ Input and output voltage ranges are from rail-to-rail.
- ▶ Transconductance gain nearly constant with V_{cm} .

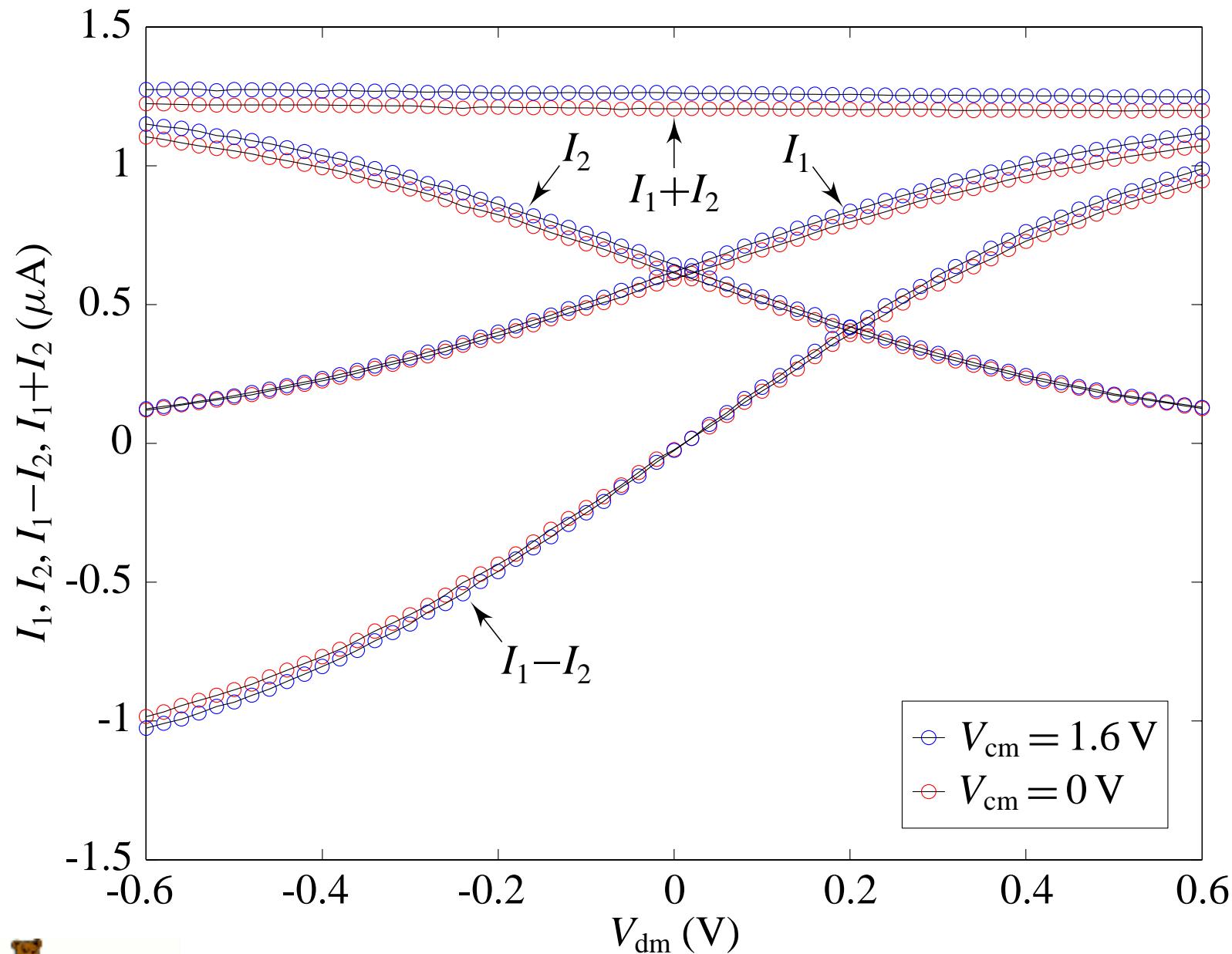
Output Currents vs. V_{dm} ($I_b = 116 \mu\text{A}$)



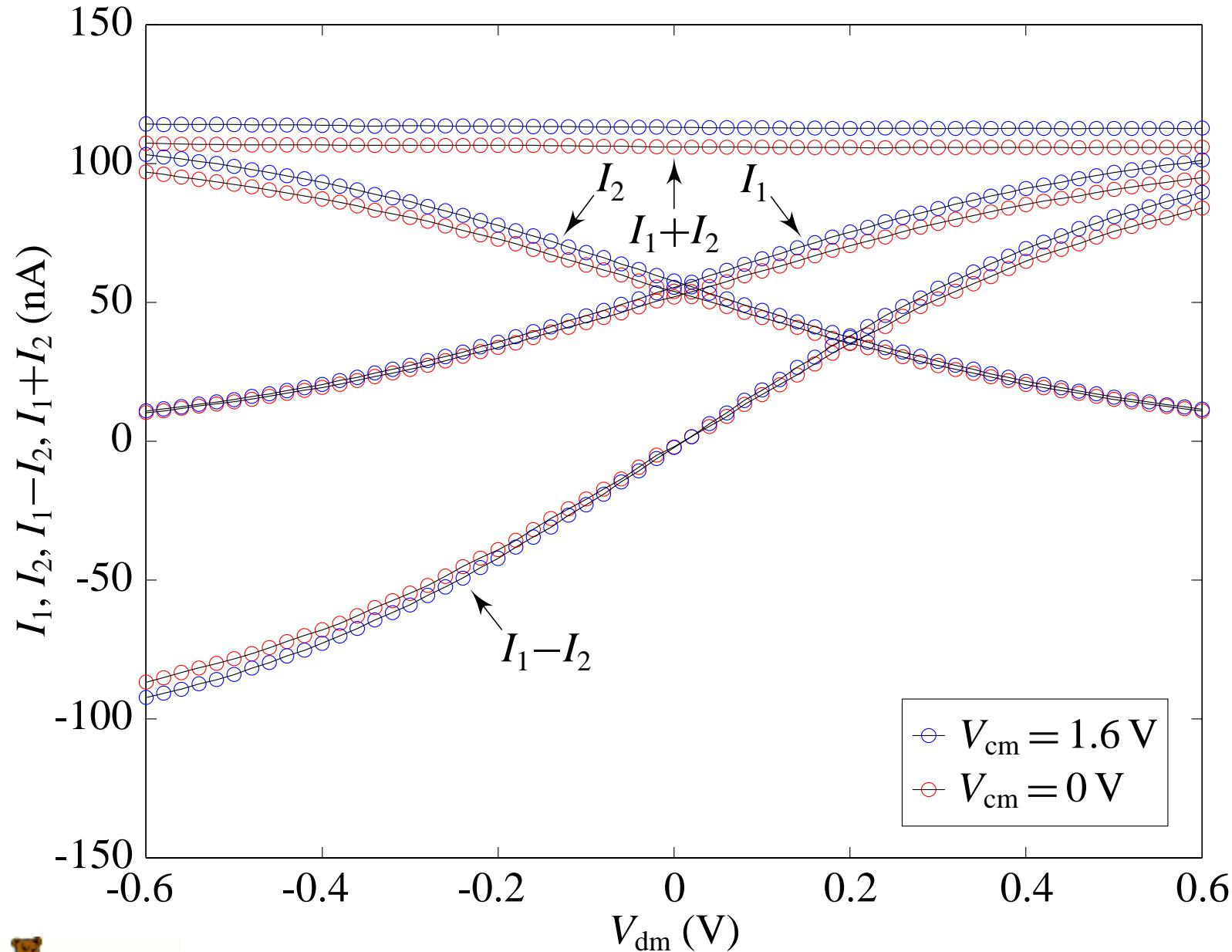
Output Currents vs. V_{dm} ($I_b = 10.7 \mu\text{A}$)



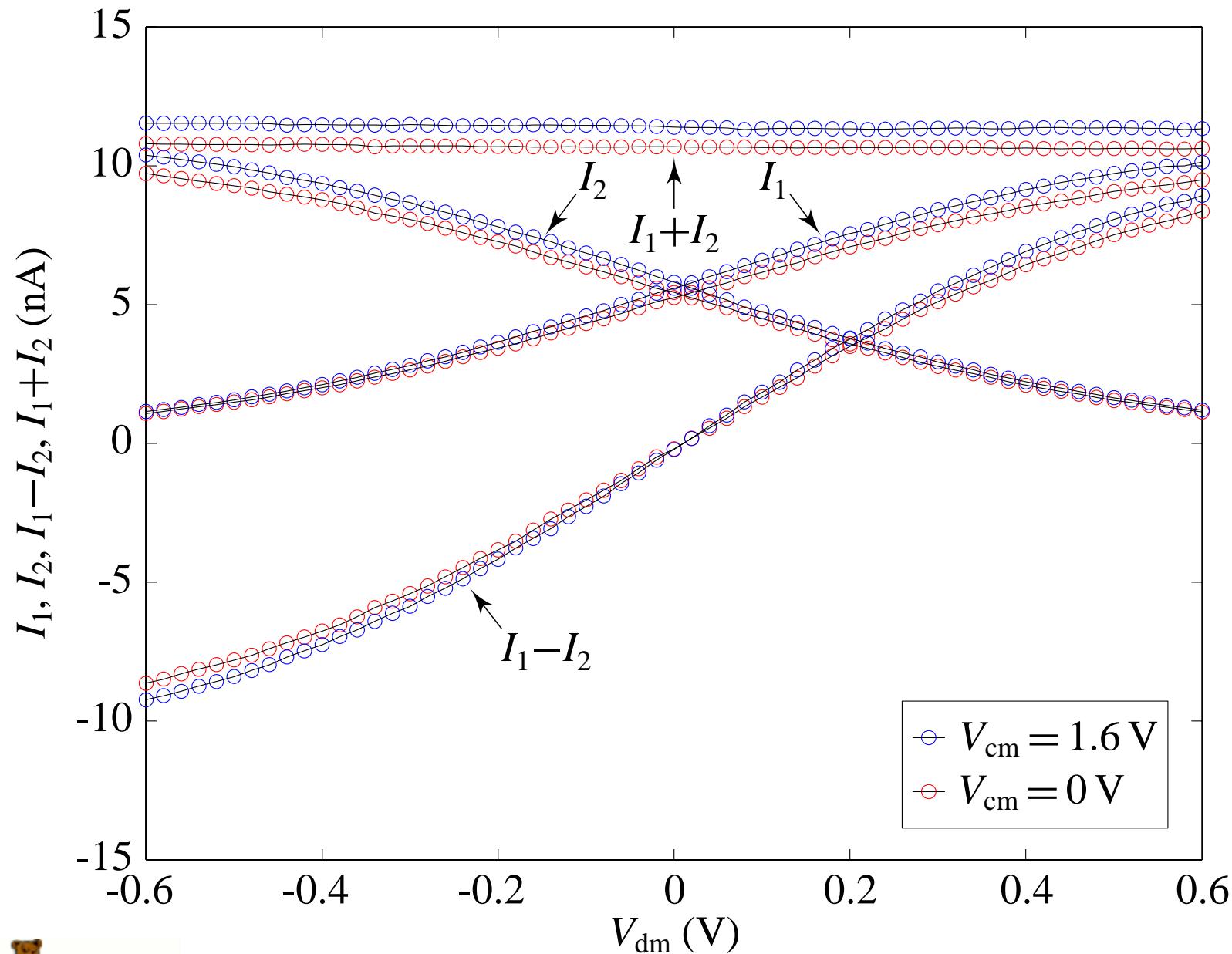
Output Currents vs. V_{dm} ($I_b = 1.23 \mu\text{A}$)



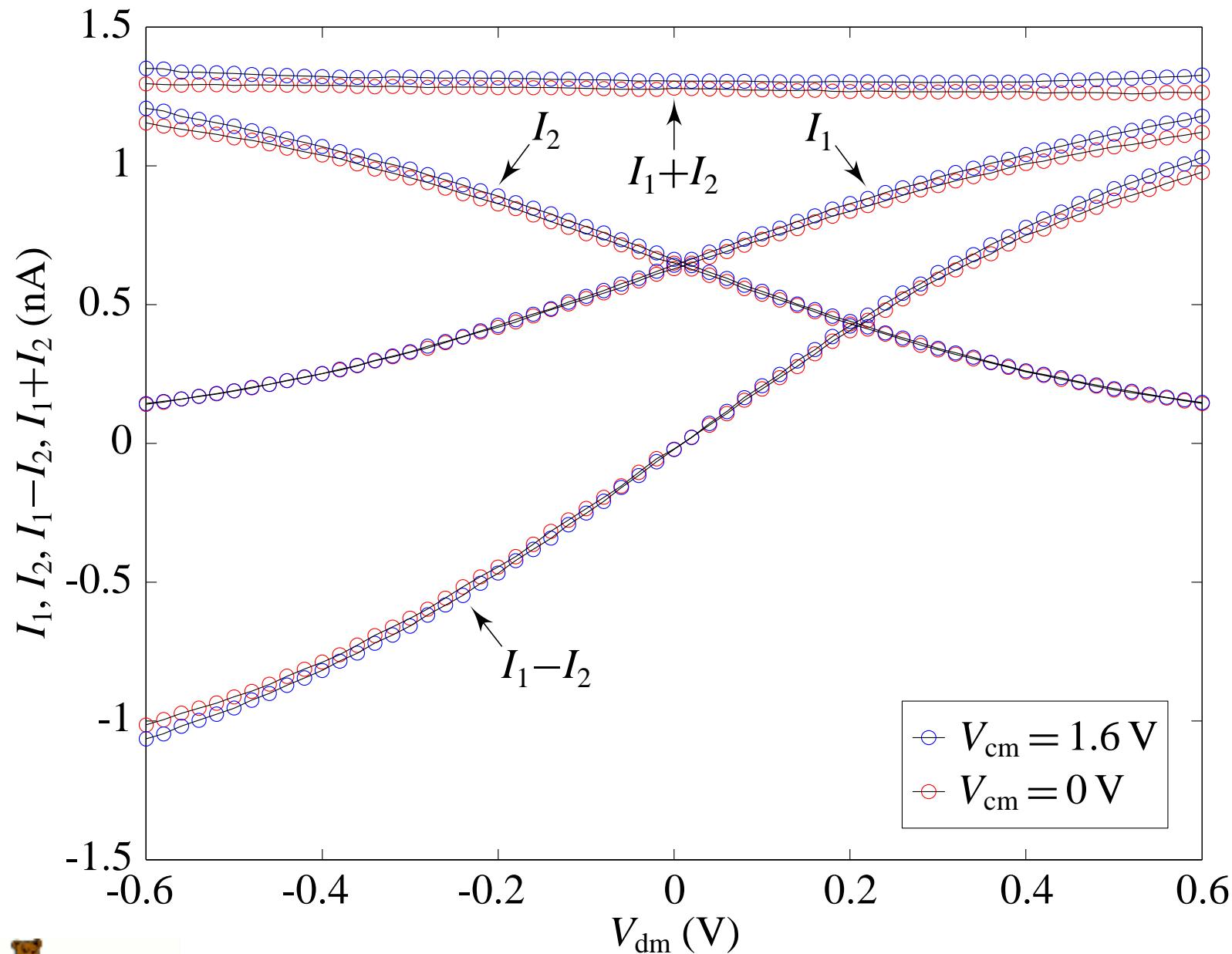
Output Currents vs. V_{dm} ($I_b = 110 \text{ nA}$)



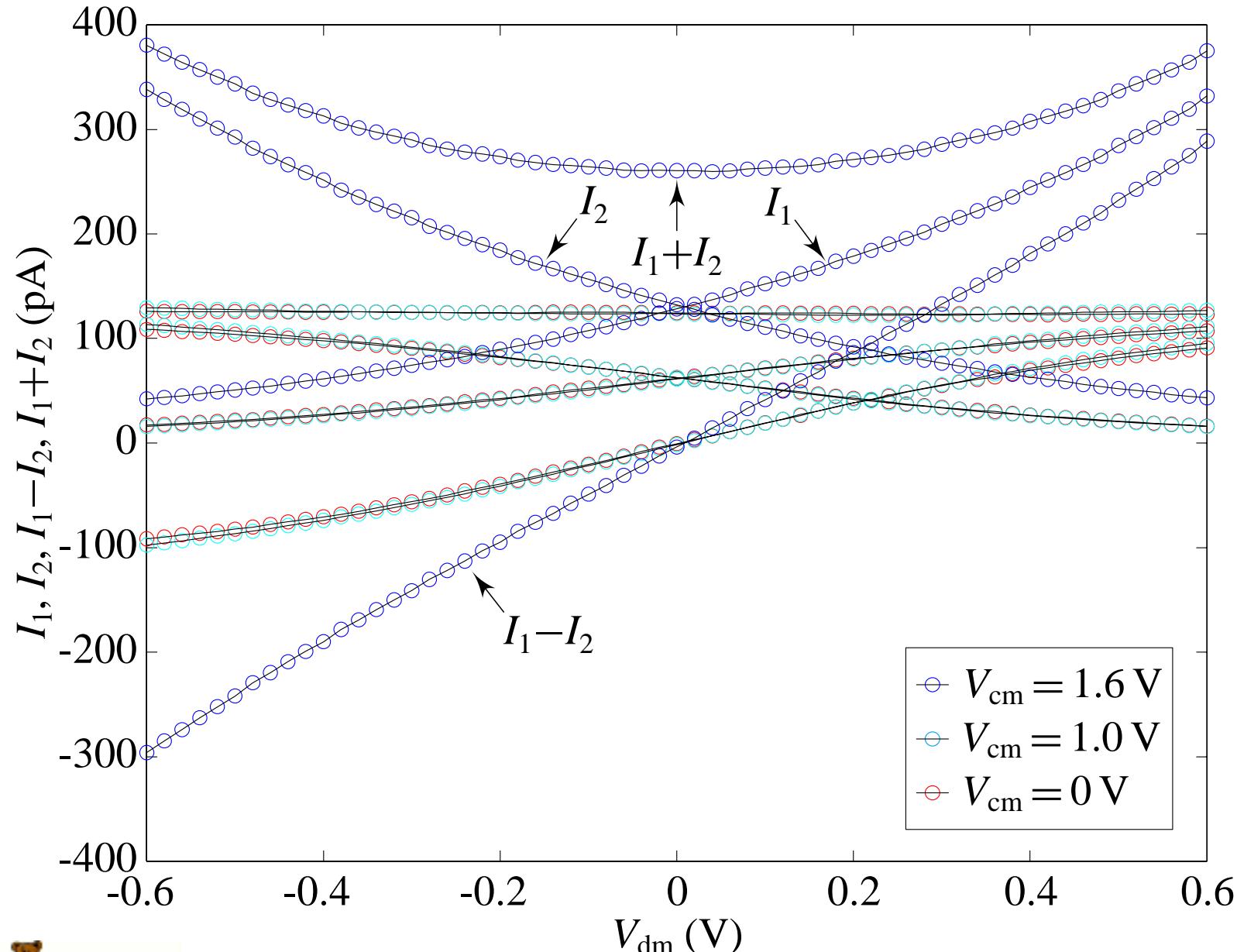
Output Currents vs. V_{dm} ($I_b = 11.1 \text{ nA}$)



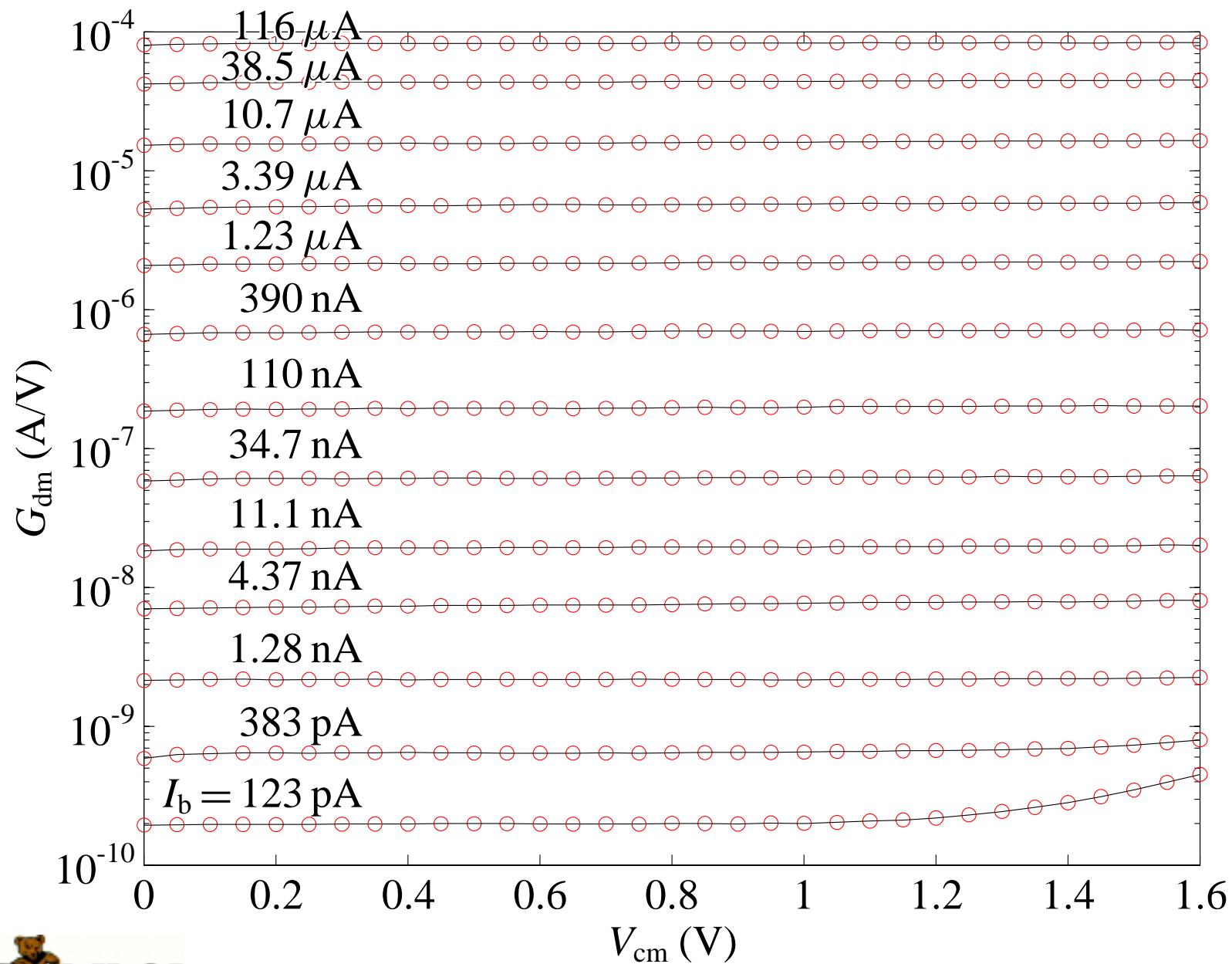
Output Currents vs. V_{dm} ($I_b = 1.28 \text{ nA}$)



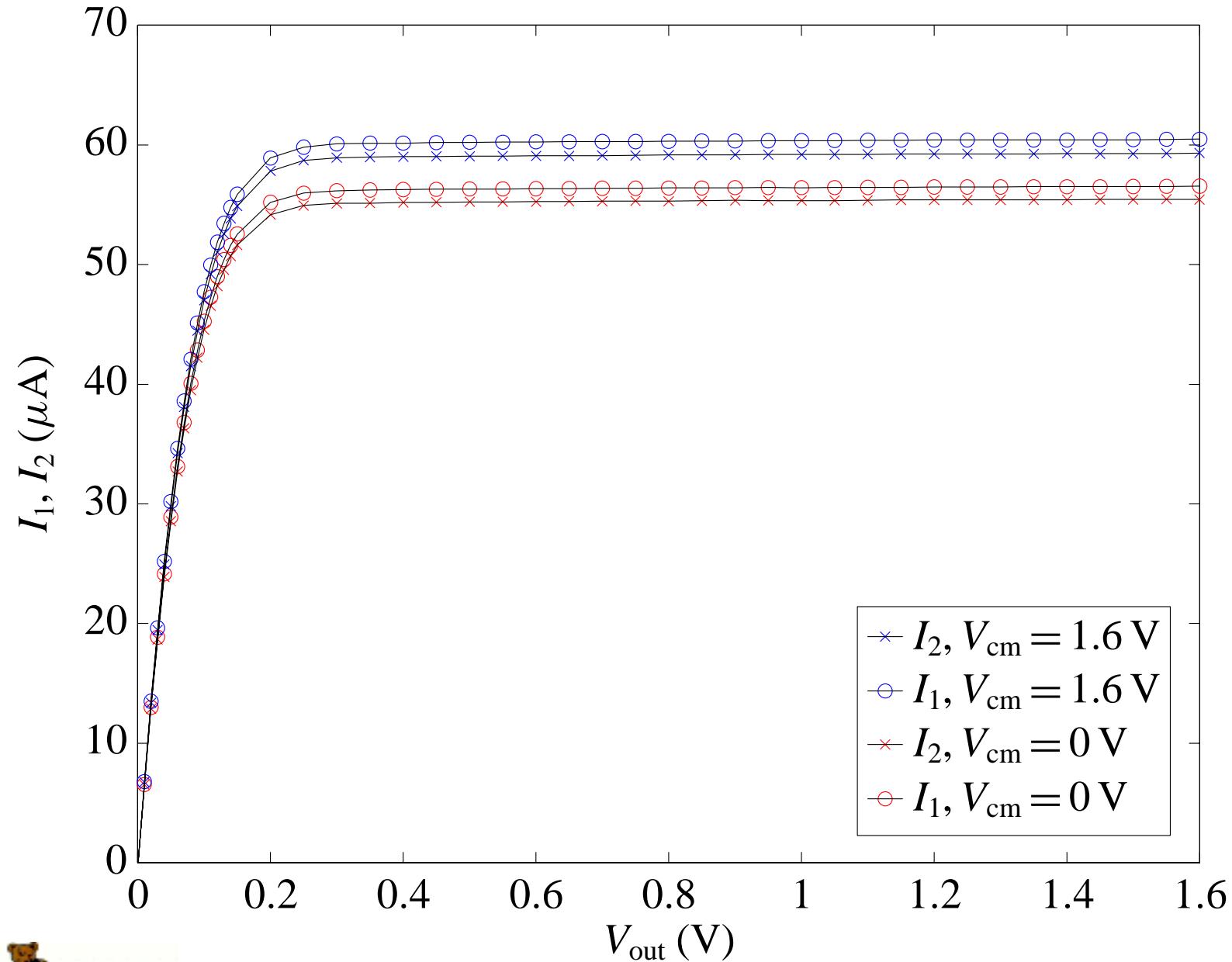
Output Currents vs. V_{dm} ($I_b = 123 \text{ pA}$)



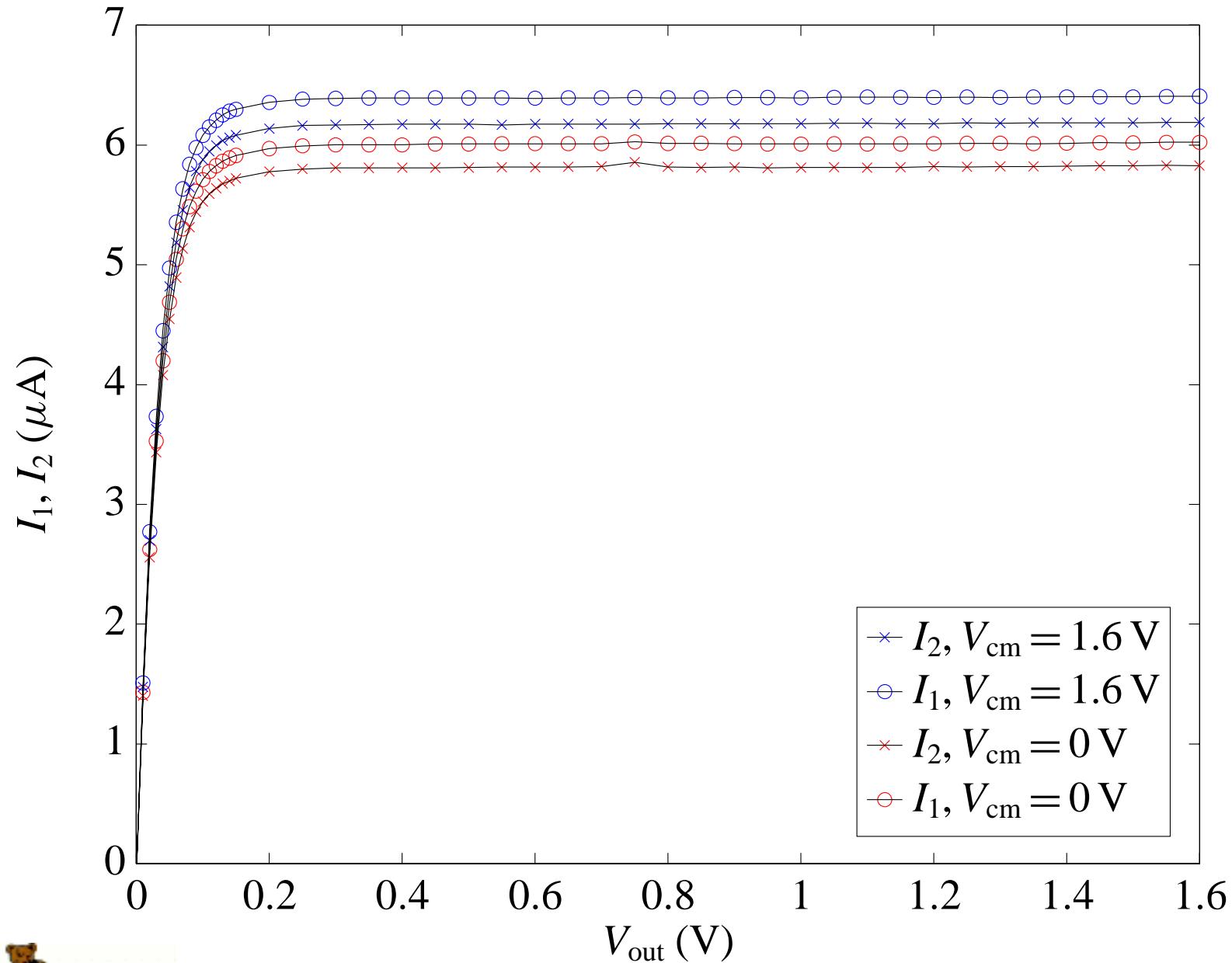
Transconductance Gain vs. V_{cm}



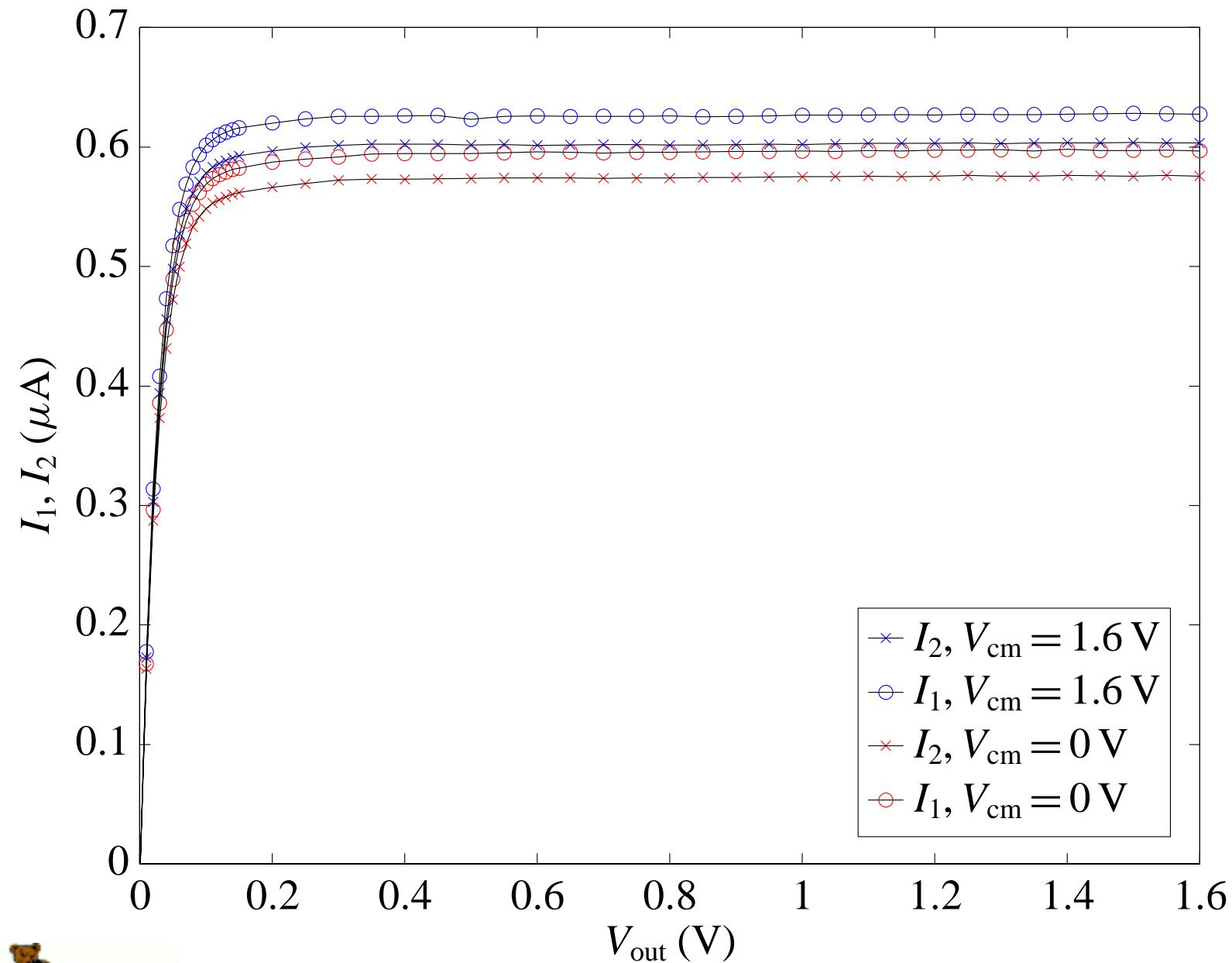
Output Currents vs. V_{out} ($I_b = 116 \mu\text{A}$)



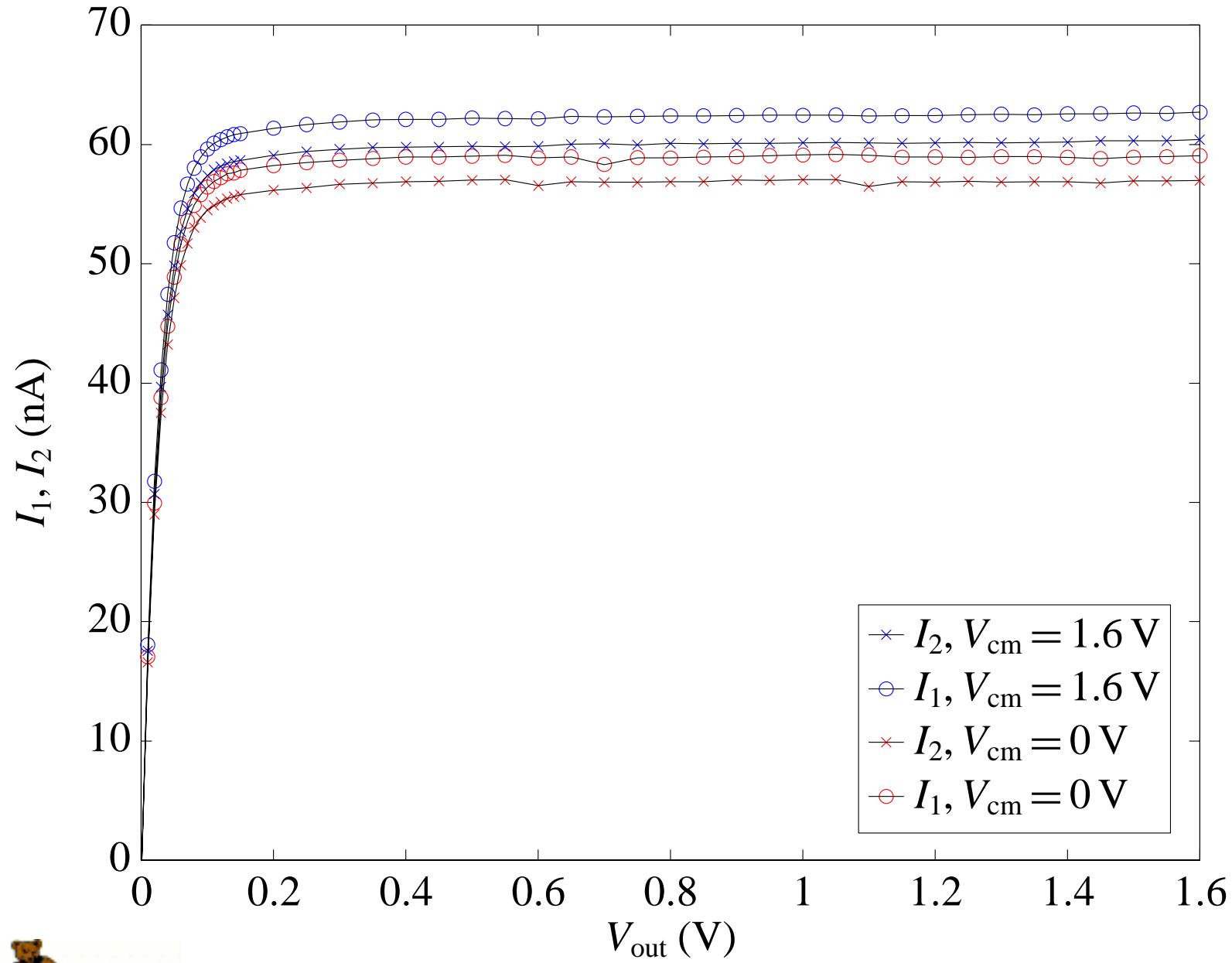
Output Currents vs. V_{out} ($I_b = 10.7 \mu\text{A}$)



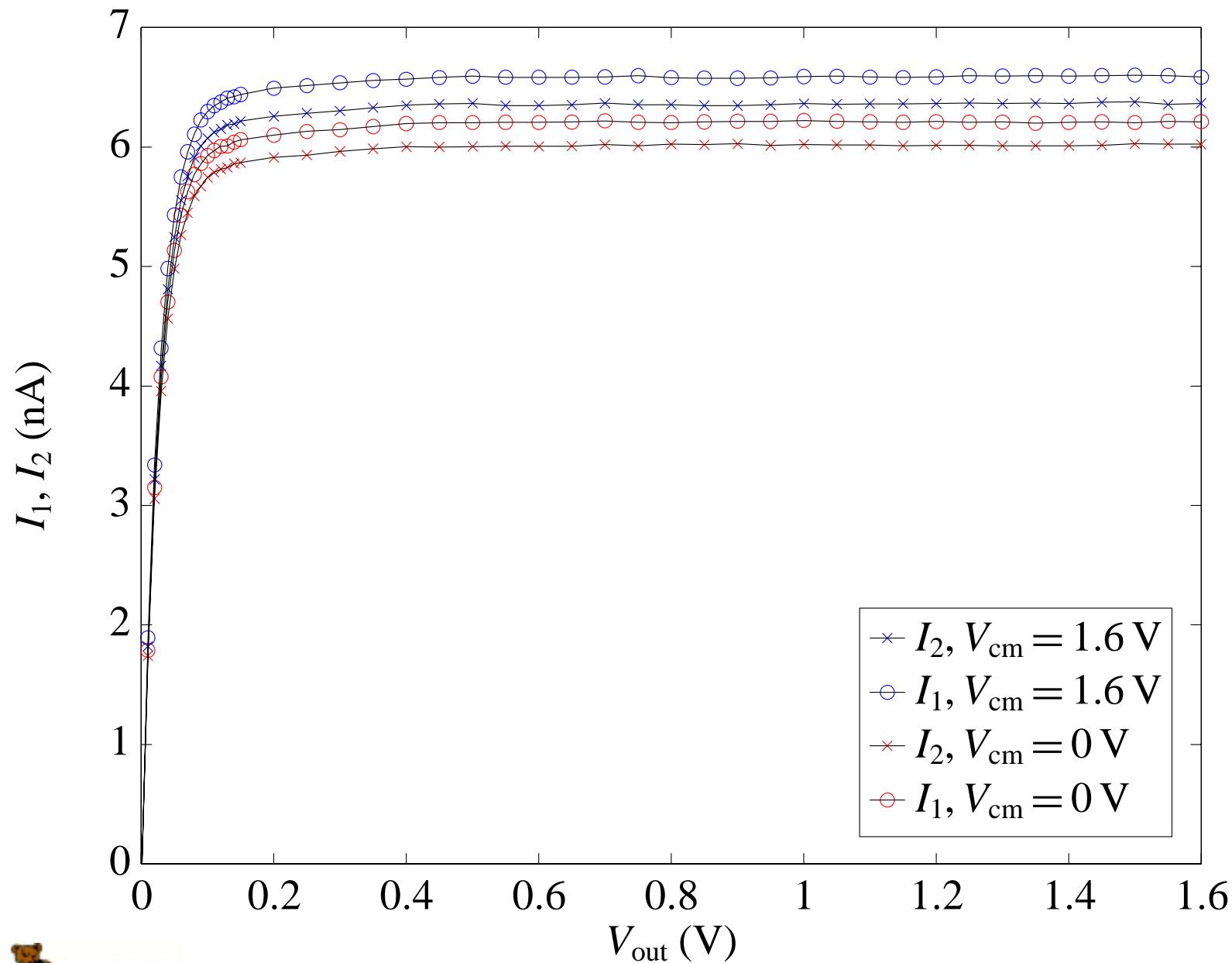
Output Currents vs. V_{out} ($I_b = 1.23 \mu\text{A}$)



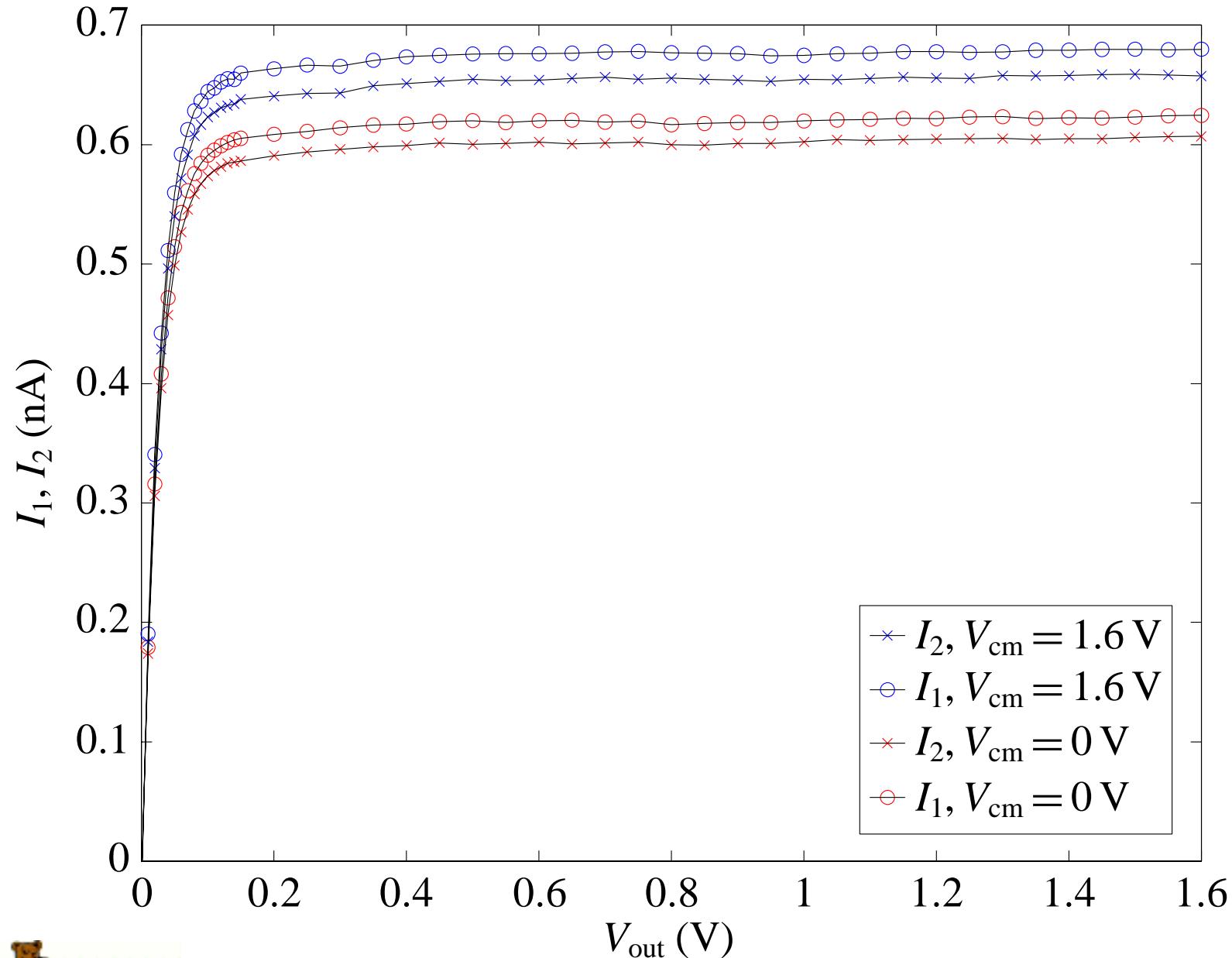
Output Currents vs. V_{out} ($I_b = 110 \text{ nA}$)



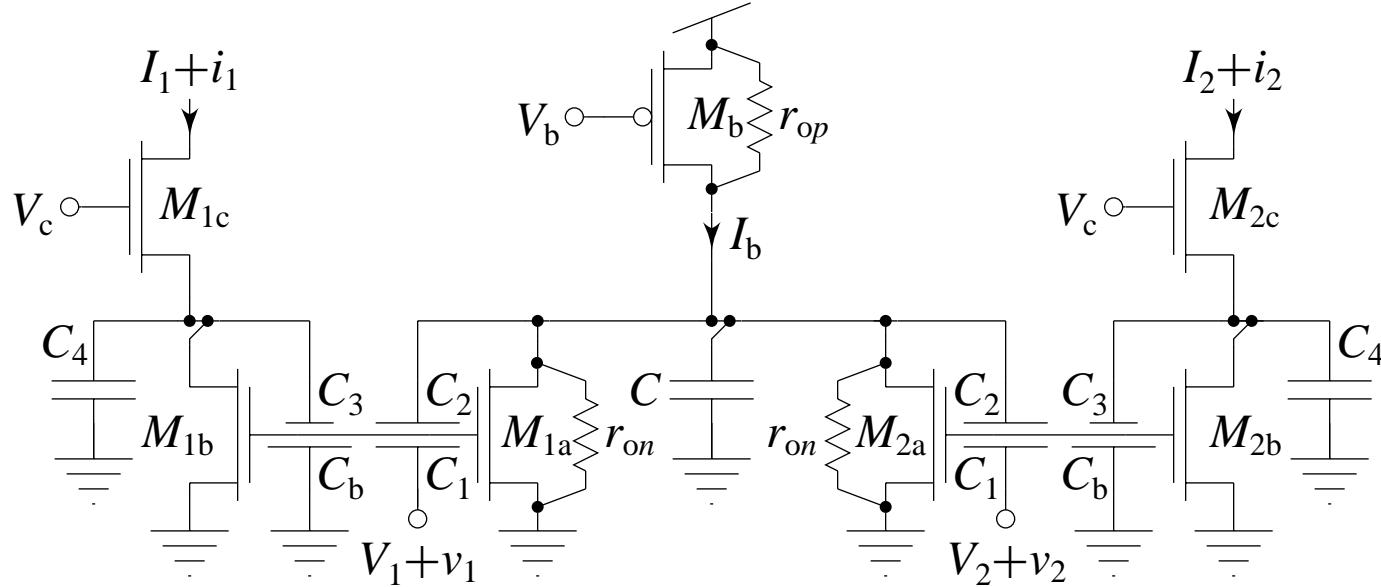
Output Currents vs. V_{out} ($I_b = 11.1 \text{ nA}$)



Output Currents vs. V_{out} ($I_b = 1.28 \text{ nA}$)



Incremental High-Frequency Analysis

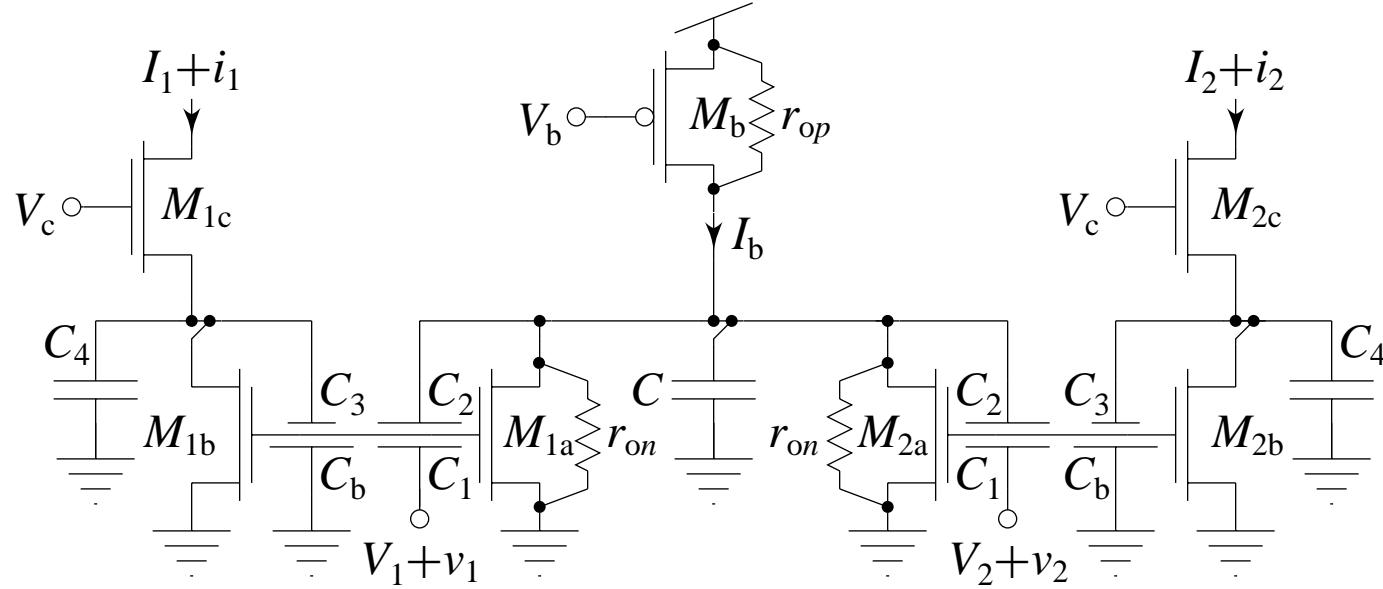


► Given that $g_m(r_{on}\|2r_{op}) \gg 1$ and $C_3 \ll C_2$, we can show that

$$i_{dm} \equiv i_1 - i_2 = g_m \frac{C_1}{C_T} \frac{1 - sC_3/g_m}{1 + s(C_3 + C_4)/g_s} v_{dm}$$

where $C_T \equiv C_1 + C_2 + C_3 + C_b$.

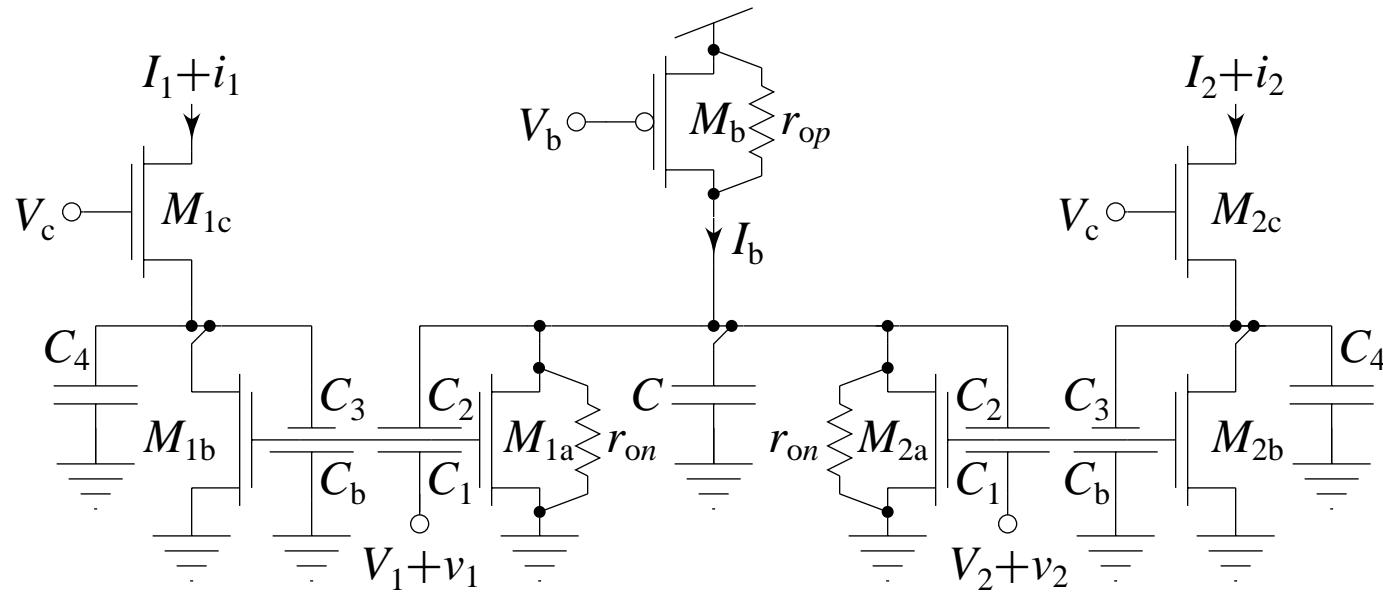
Incremental High-Frequency Analysis



...and that

$$\begin{aligned}
 i_{\text{cm}} &\equiv \frac{i_1 + i_2}{2} \\
 &= \frac{C_1/C_2}{r_{\text{on}}\|2r_{\text{op}}} \frac{(1 - sC_3/g_m)(1 + s(r_{\text{on}}\|2r_{\text{op}})(C_2 + C/2))}{(1 + s(C_3 + C_4)/g_s)(1 + s(C_2\|(C_1 + C_3 + C_b))/(g_m C_2/C_T))} v_{\text{cm}}
 \end{aligned}$$

Incremental High-Frequency Analysis



...and so

$$\text{CMRR} \equiv \frac{i_{\text{dm}}/v_{\text{dm}}}{i_{\text{cm}}/v_{\text{cm}}} = g_m \left(r_{on} \| 2r_{op} \right) \frac{C_2}{C_T} \frac{\left(1 + s(C_2 \parallel (C_1 + C_3 + C_b)) / (g_m C_2 / C_T) \right)}{\left(1 + s(r_{on} \| 2r_{op}) (C_2 + C/2) \right)}$$