

A Simple Variable-Width CMOS Bump Circuit

Bradley A. Minch

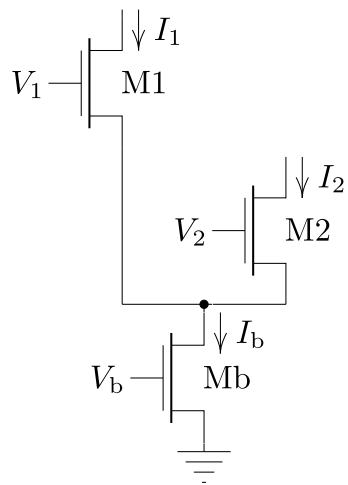
Mixed Analog-Digital VLSI Circuits & Systems Lab
Franklin W. Olin College of Engineering
Needham, MA 02492

bradley.minch@olin.edu

24 May 2016

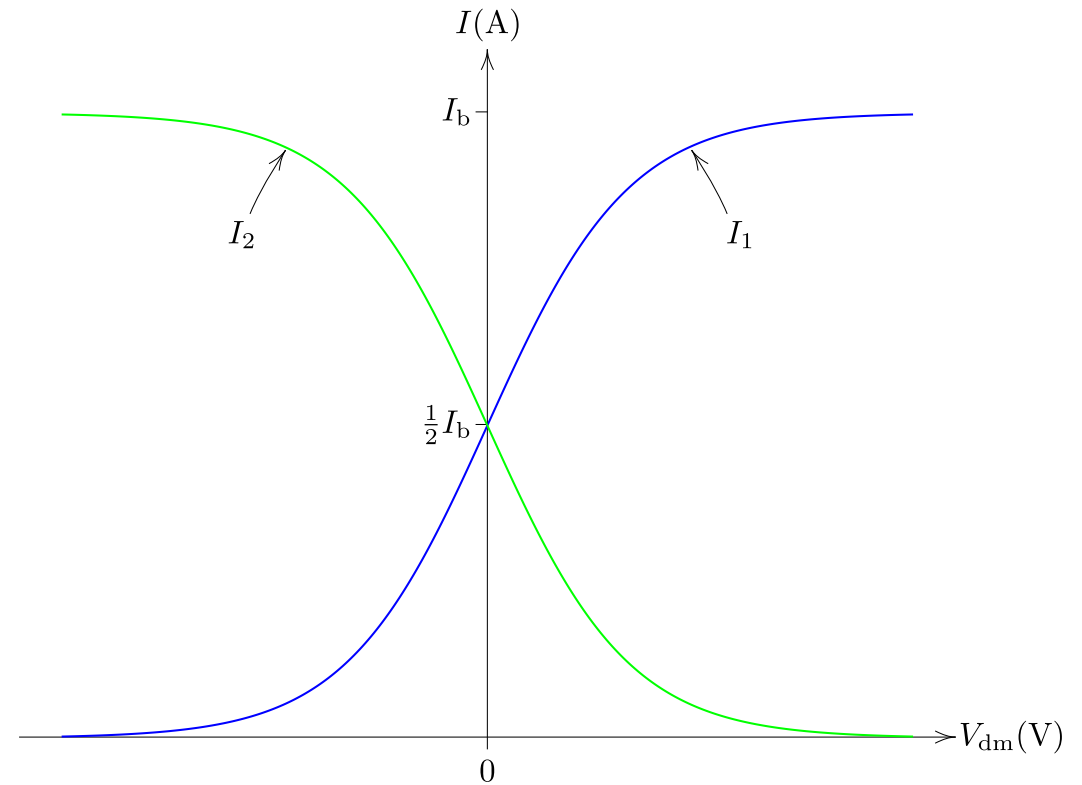


Delbrück's Original Bump Circuit

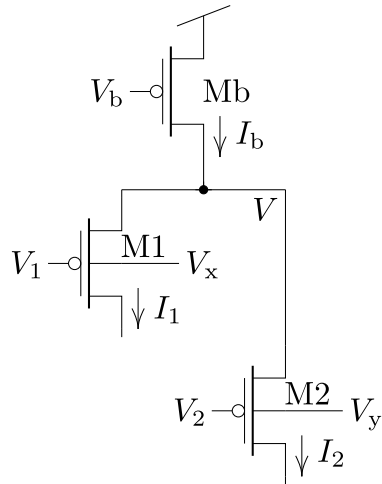


$$I_{1,2} = \frac{I_b}{1 + e^{\mp \kappa V_{dm}/U_T}}$$

$$V_{dm} = V_1 - V_2$$



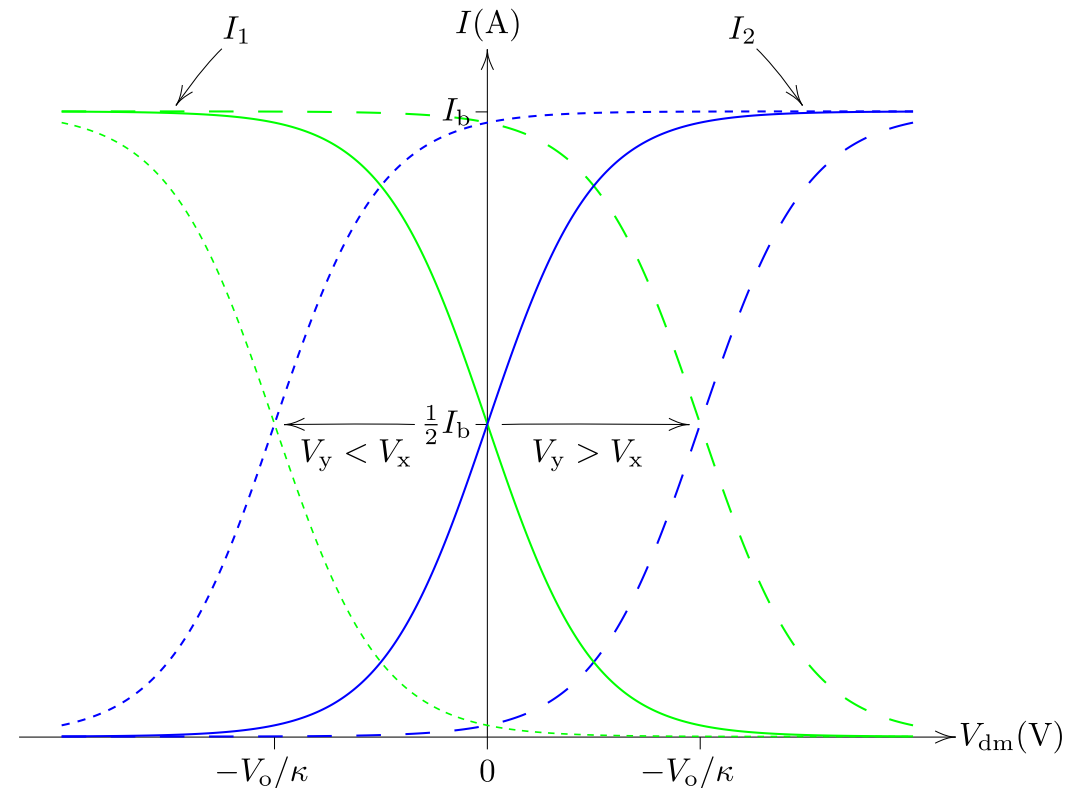
Asymmetric Differential Pair



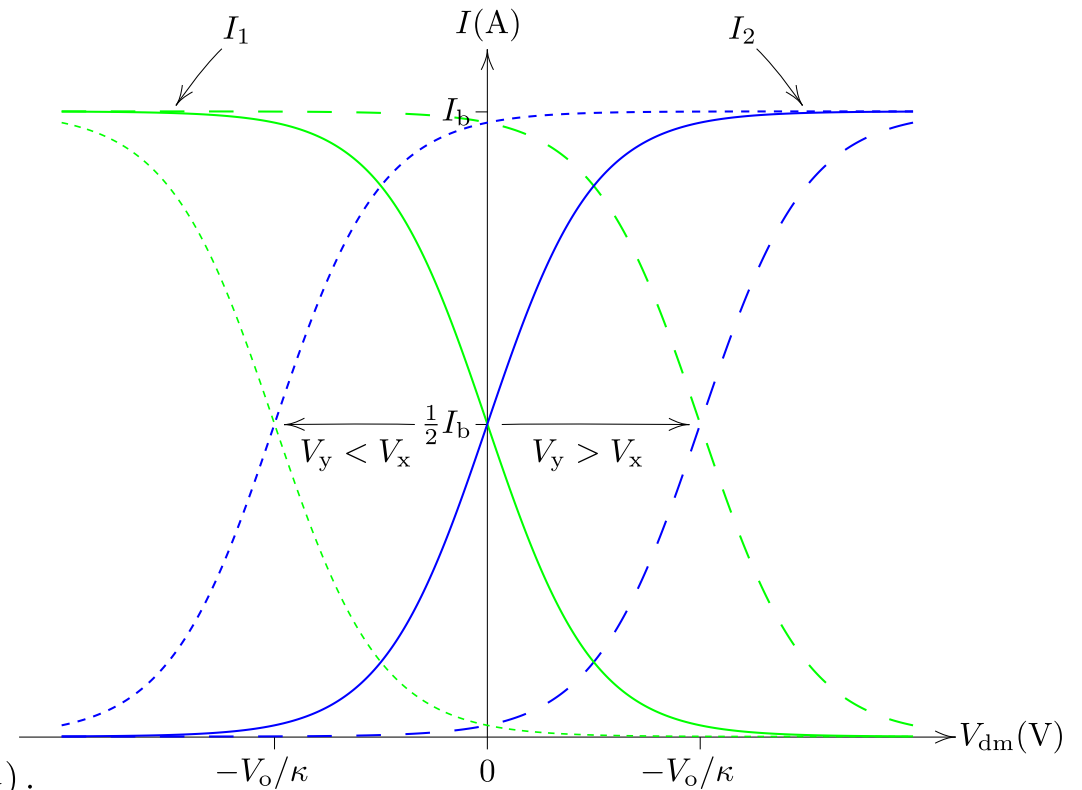
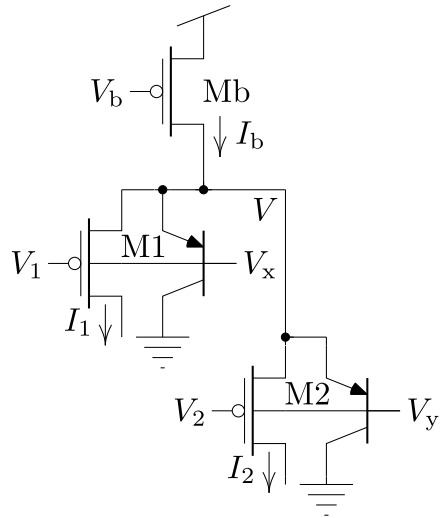
$$I_{1,2} = \frac{I_b}{1 + e^{\pm V_o/U_T} e^{\pm \kappa V_{dm}/U_T}}$$

$$V_{dm} = V_1 - V_2, \quad V_o = (1 - \kappa)(V_x - V_y)$$

$$V \leq \min(\kappa V_1 + (1 - \kappa)V_x, \kappa V_2 + (1 - \kappa)V_y) + \kappa(V_{dd} - V_b)$$



Asymmetric Differential Pair



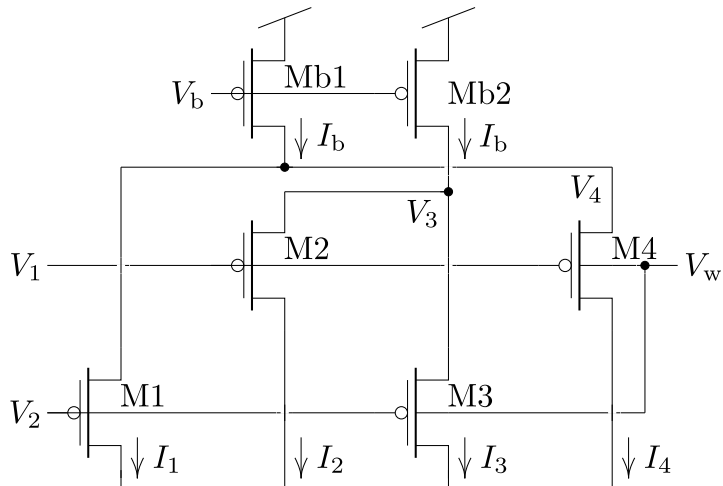
To ensure proper operation, we should have

$$V < \min(V_x, V_y),$$

which we can guarantee if we require

$$\min(\kappa V_1 + (1 - \kappa) V_x, \kappa V_2 + (1 - \kappa) V_y) + \kappa (V_{dd} - V_b) < \min(V_x, V_y).$$

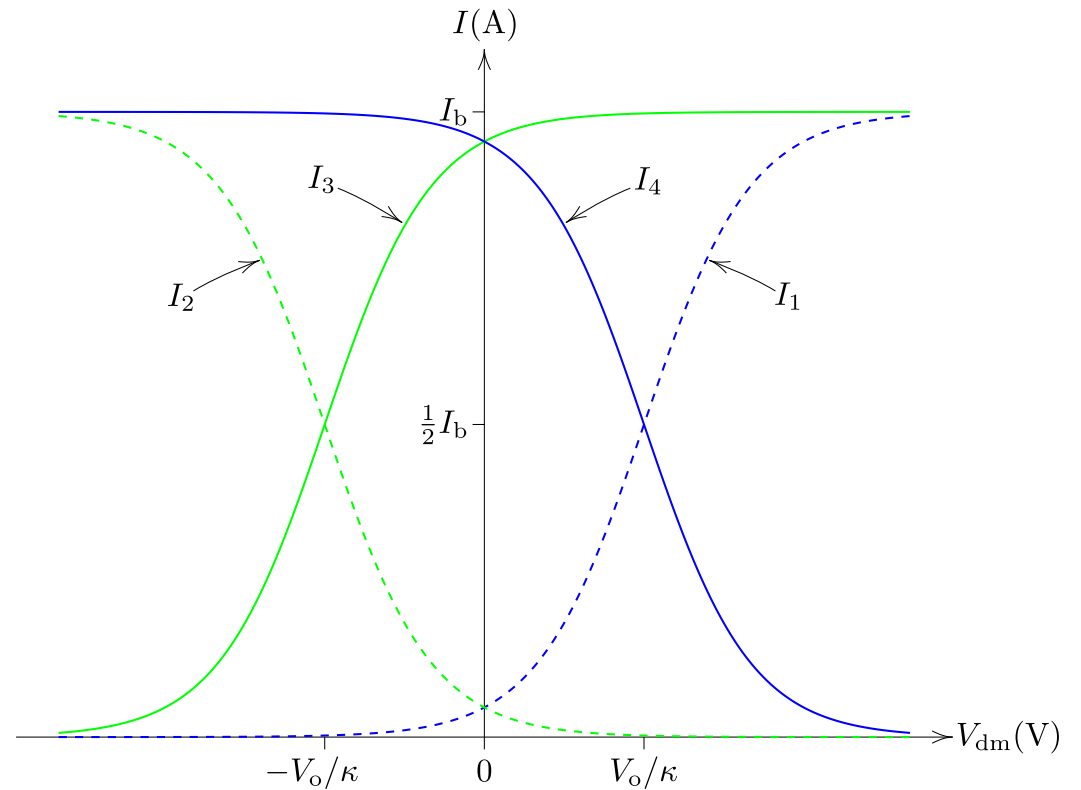
Principle of Circuit Operation



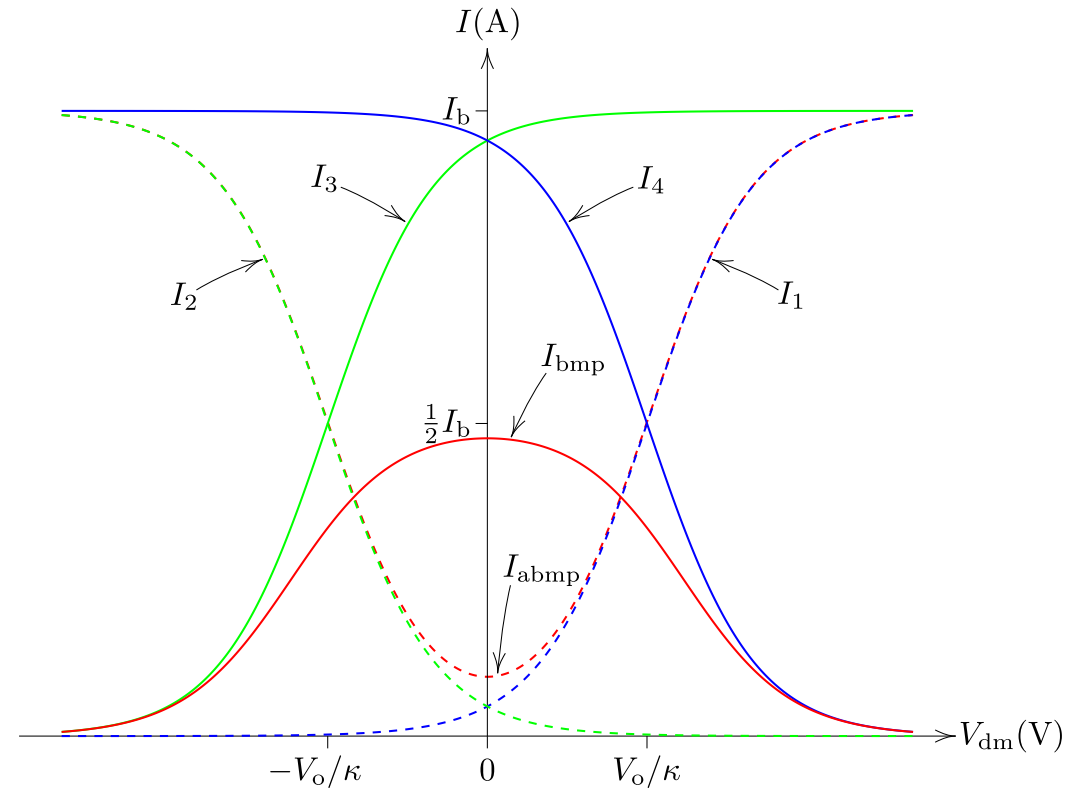
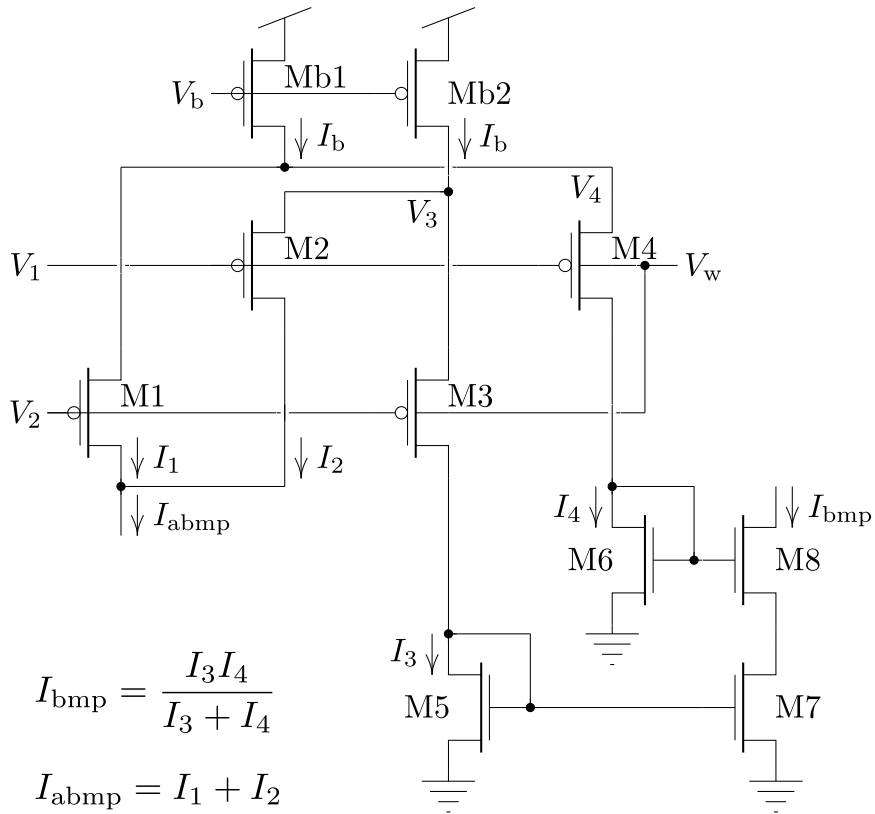
$$I_{1,2} = \frac{I_b}{1 + e^{+V_o/U_T} e^{\mp \kappa V_{dm}/U_T}}$$

$$I_{3,4} = \frac{I_b}{1 + e^{-V_o/U_T} e^{\mp \kappa V_{dm}/U_T}}$$

$$V_{dm} = V_1 - V_2, \quad V_o = (1 - \kappa)(V_{dd} - V_w)$$



Principle of Circuit Operation



Principle of Circuit Operation

Given that

$$I_{1,2} = \frac{I_b}{1 + e^{+V_o/U_T} e^{\mp \kappa V_{dm}/U_T}} \quad \text{and}$$

$$I_{3,4} = \frac{I_b}{1 + e^{-V_o/U_T} e^{\mp \kappa V_{dm}/U_T}},$$

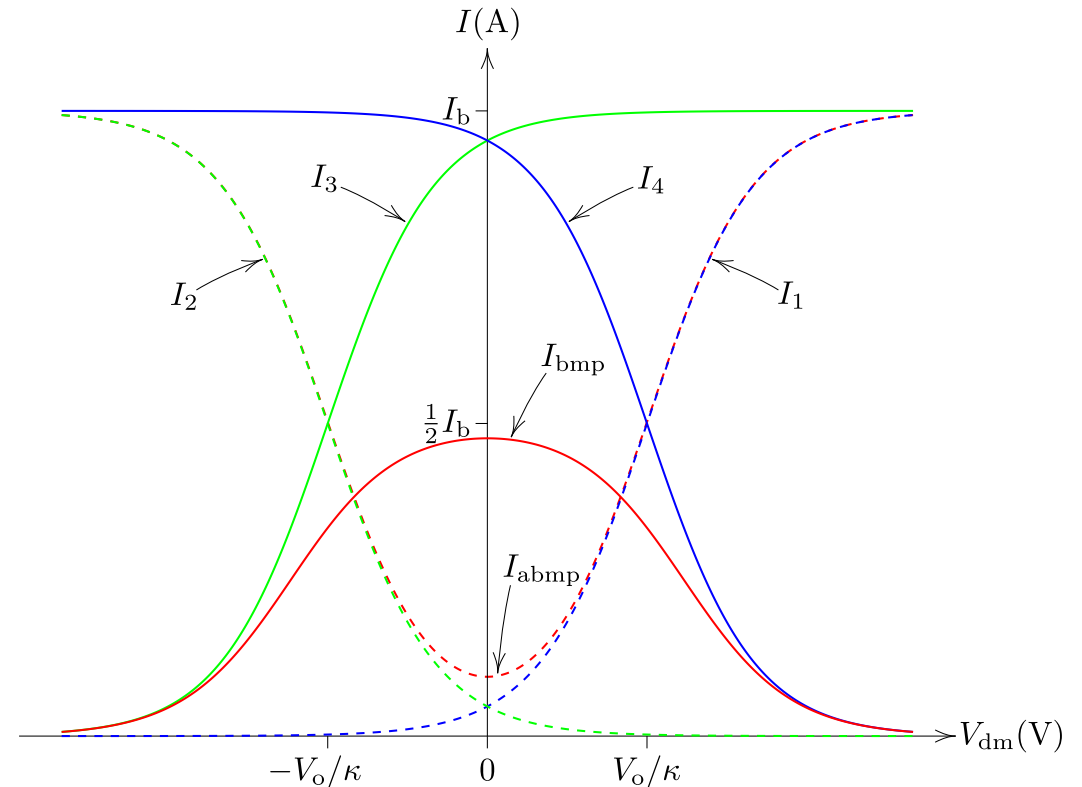
where

$$V_{dm} = V_1 - V_2 \quad \text{and} \quad V_o = (1 - \kappa)(V_{dd} - V_w),$$

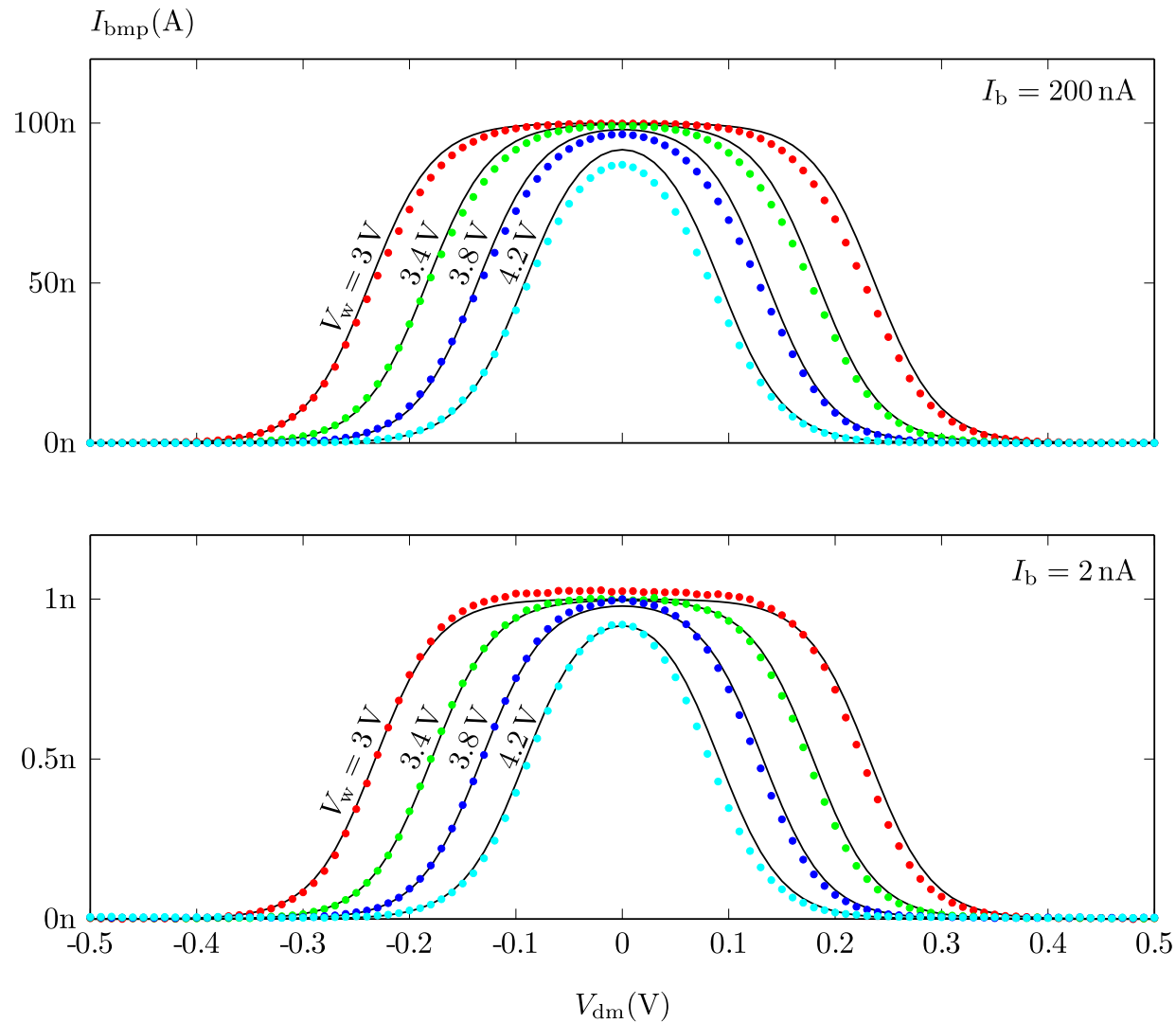
we can show that

$$I_{b\text{mp}} = \frac{I_3 I_4}{I_3 + I_4} = \frac{I_b/2}{1 + e^{-V_o/U_T} \cosh(\kappa V_{dm}/U_T)} \quad \text{and}$$

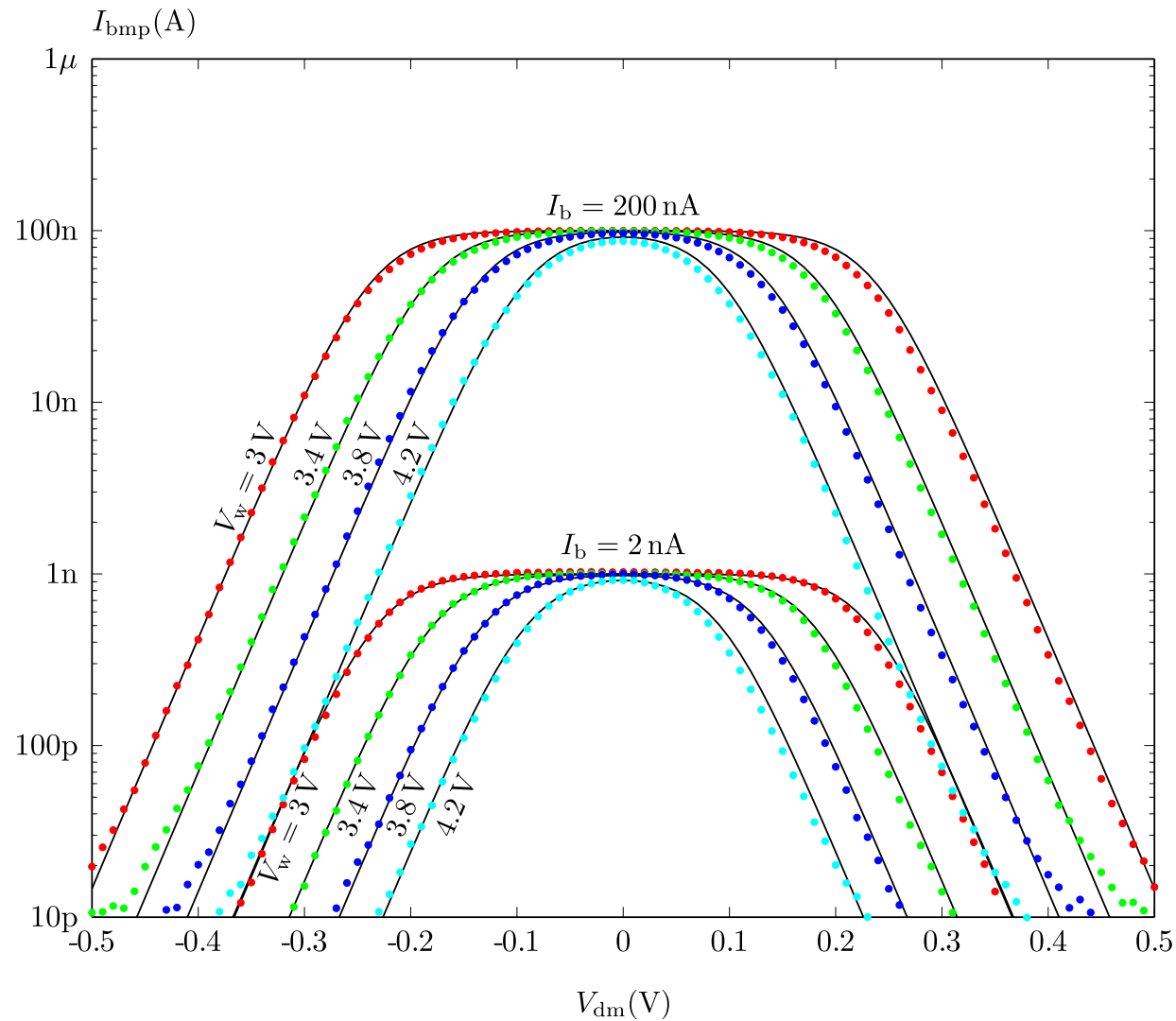
$$I_{ab\text{mp}} = I_1 + I_2 = I_b \cdot \frac{e^{-V_o/U_T} \cosh(\kappa V_{dm}/U_T)}{1 + e^{-V_o/U_T} \cosh(\kappa V_{dm}/U_T)}.$$



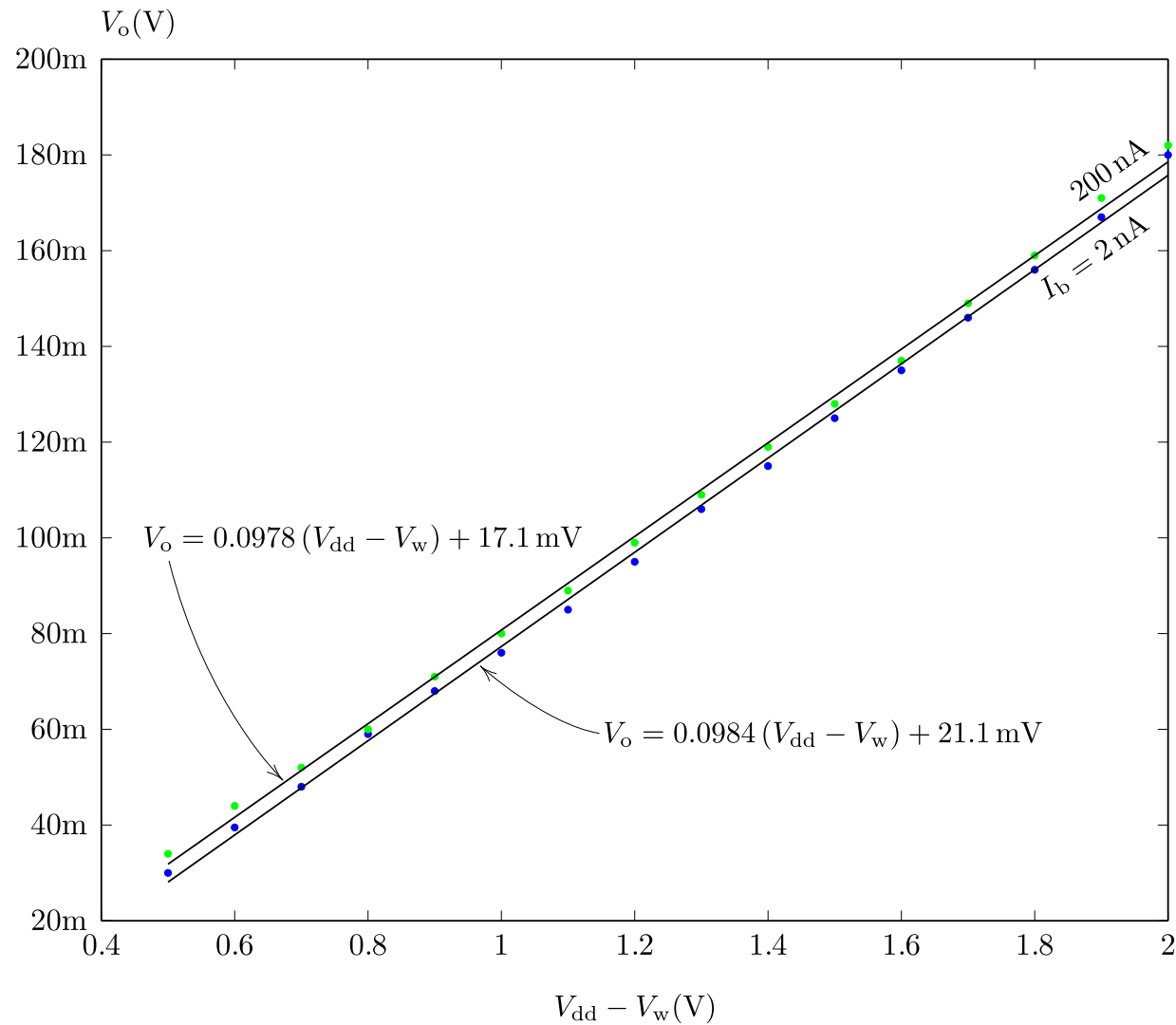
Bump Circuit Transfer Characteristics



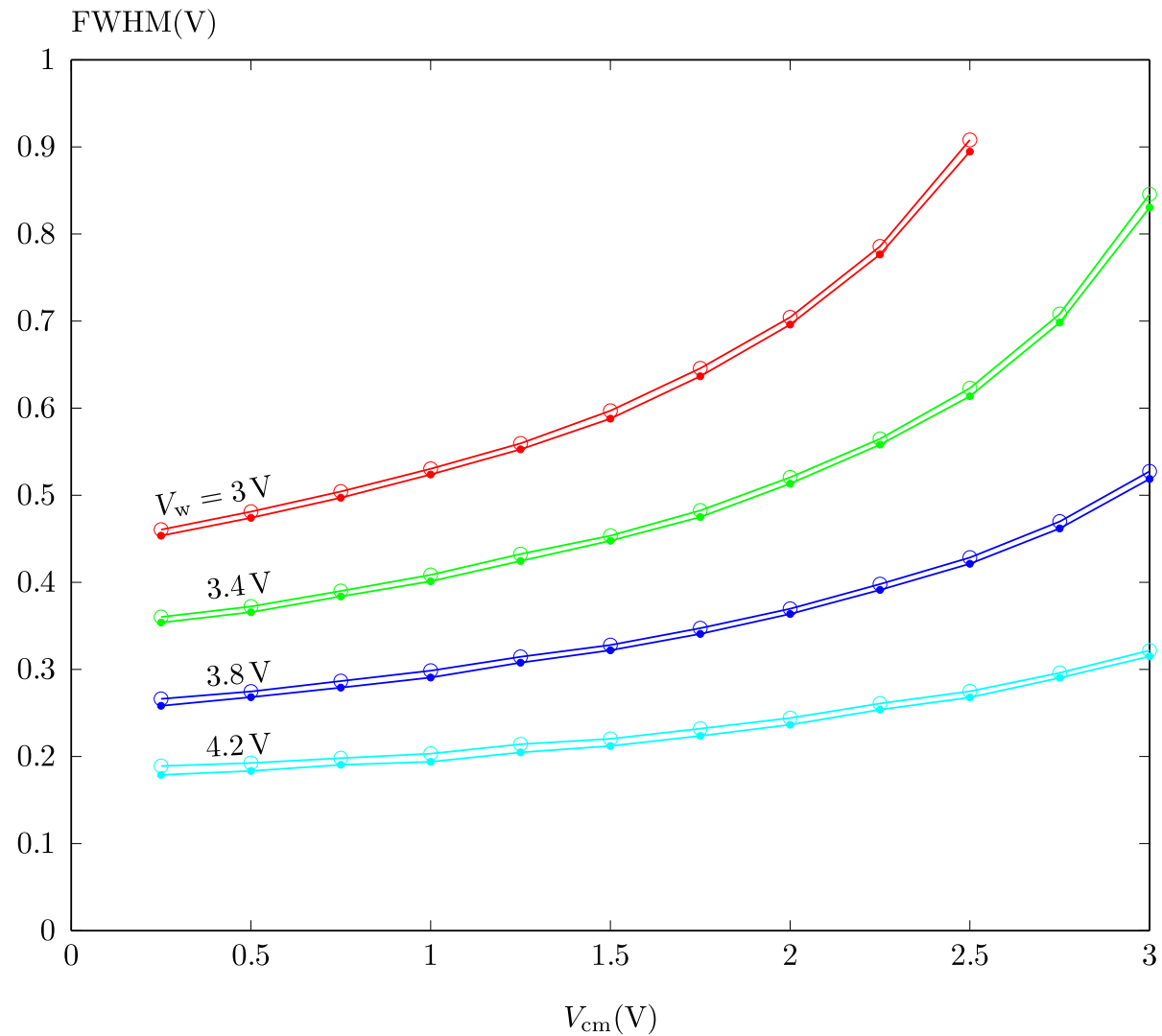
Bump Circuit Transfer Characteristics



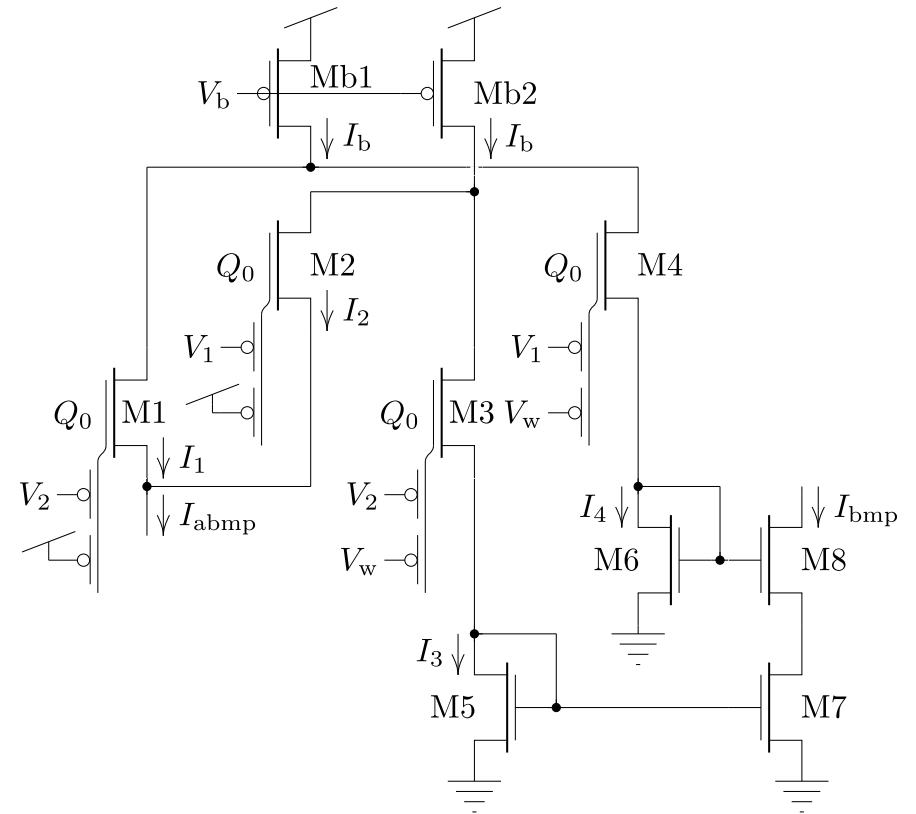
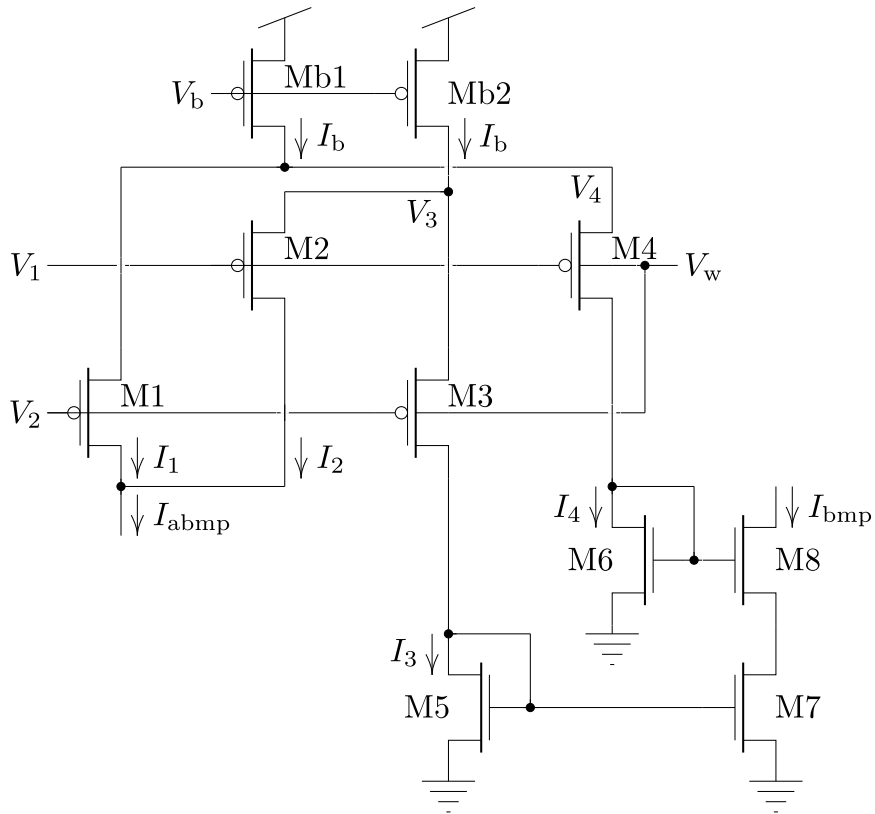
Bump Circuit Transfer Characteristics



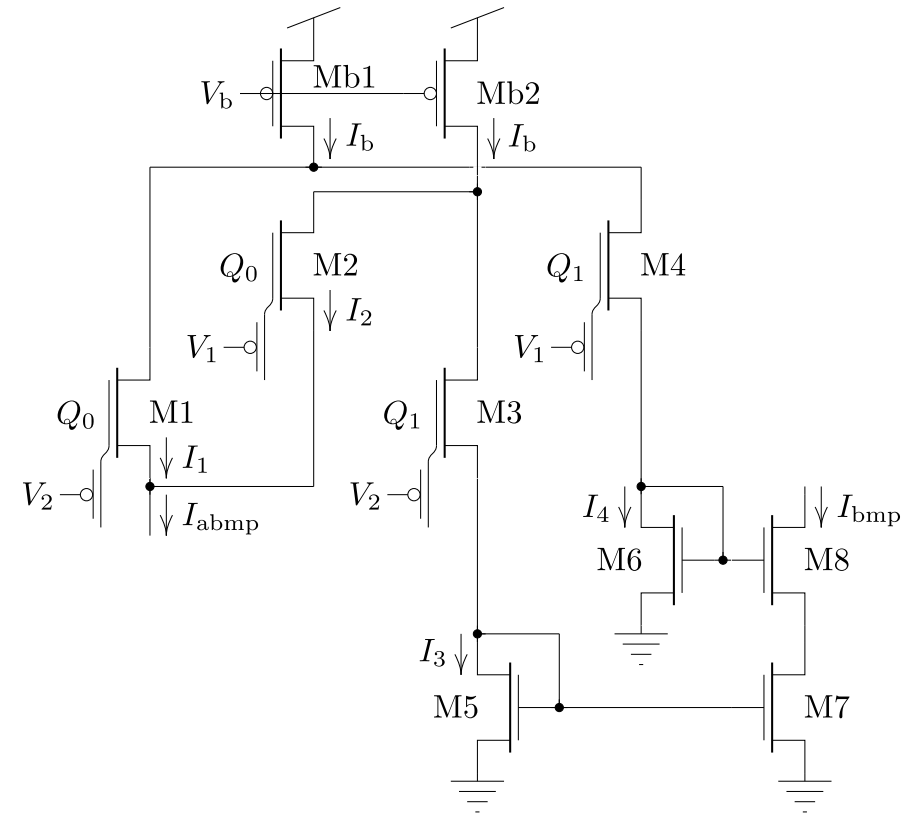
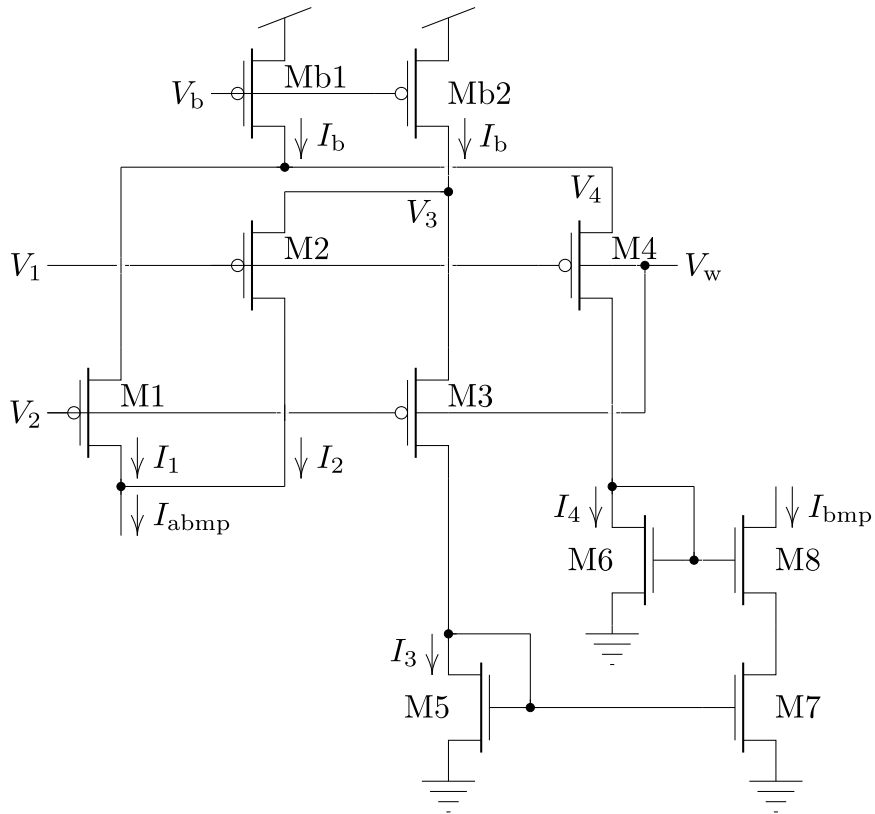
Effect of Common-Mode Input Voltage



Floating-Gate Variations on a Theme



Floating-Gate Variations on a Theme



Floating-Gate Variations on a Theme

